

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	850	703/14.ccls.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/09/19 10:45
L2	5	("5426651" "5926622" "6049662" "6074426" "6205559").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/19 11:50
L3	777	model adj check\$3	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/09/19 11:51
L4	398	L3 and (trace or path or backtrack\$3)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 12:49
L5	15	("5243538" "5349659" "5377201" "5469367" "5481473" "5485471" "5497334" "5522063" "5526514" "5528508" "5649165" "5671399" "5787006" "5801958" "5805462").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/19 12:24
L6	5	("4688223" "5027353" "5187712" "5323400" "5414716").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/19 12:26
L7	14	("4930072" "5163016" "5291427" "5463563" "5513122" "5606698" "5680332" "5752241" "5764951" "5937195" "5995739" "5999717" "6009250" "6175946").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/19 12:28
L8	117	L4 and distance	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 12:53
L9	1	L8 and hamming	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 12:50
L10	1	L4 and (multiple near counterexamples)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 12:53
L11	16	L4 and (multiple near traces)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 12:55

L12	83	multiple near proof\$1	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 12:55
L13	15	L12 and (logic or model)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 13:02
L14	2	"5999717".pn.	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 13:04
L15	2	"5485471".pn.	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 13:12
L16	2	"6587998".pn.	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 13:18
L17	1057	714/25.ccls.	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 13:19
L18	679	L17 and @ad<"20010122"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 13:19
L19	168	L18 and model	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 13:19

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4	disjoint adj trace\$1	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 15:28
L2	291	disjoint adj path\$2	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 15:28
L3	183	L2 and (theorem or model or logic)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2005/09/19 15:29


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 Terms used **disjoint proofs**

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1 [Parsing expression grammars: a recognition-based syntactic foundation](#)

Bryan Ford

 January 2004 **ACM SIGPLAN Notices , Proceedings of the 31st ACM SIGPLAN-SIGACT symposium on Principles of programming languages**, Volume 39 Issue 1

 Full text available: ☒ pdf(158.06 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

For decades we have been using Chomsky's generative system of grammars, particularly context-free grammars (CFGs) and regular expressions (REs), to express the syntax of programming languages and protocols. The power of generative grammars to express ambiguity is crucial to their original purpose of modelling natural languages, but this very power makes it unnecessarily difficult both to express and to parse machine-oriented languages using CFGs. Parsing Expression Grammars (PEGs) provide an alt ...

Keywords: BNF, GTDPL, TDPL, context-free grammars, lexical analysis, packrat parsing, parsing expression grammars, regular expressions, scannerless parsing, syntactic predicates, unified grammars

2 [Session 1: Separating point sets in polygonal environments](#)

Erik D. Demaine, Jeff Erickson, Ferran Hurtado, John Iacono, Stefan Langerman, Henk Meijer, Mark Overmars, Sue Whitesides

 June 2004 **Proceedings of the twentieth annual symposium on Computational geometry**

 Full text available: ☒ pdf(301.80 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We consider the separability of two point sets inside a polygon by means of chords or geodesic lines. Specifically, given a set of red points and a set of blue points in the interior of a polygon, we provide necessary and sufficient conditions for the existence of a chord and for the existence of a geodesic path which separate the two sets when they exist we also derive efficient algorithms for their obtention. We study as well the separation of the two sets using a minimum number of pairwise no ...

Keywords: chords, geodesics, polygons, separability

3 [Optimal length tree-like resolution refutations for 2SAT formulas](#)

K. Subramani

 April 2004 **ACM Transactions on Computational Logic (TOCL)**, Volume 5 Issue 2

 Full text available: ☒ pdf(37.15 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#), [review](#)

In this article, we exploit the graphical structure of 2SAT formulas to show that the shortest

tree-like resolution refutation of an unsatisfiable 2SAT formula can be determined in polynomial time.

Keywords: 2SAT-formulas, Resolution, tree-like proofs

4 Referencing and Retention in Block-Structured Coroutines

Gary Lindstrom, Mary Lou Soffa

July 1981 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,
Volume 3 Issue 3

Full text available:  [pdf\(1.62 MB\)](#)


Additional Information: [full citation](#), [references](#), [index terms](#)



5 Session 3A: Efficient estimation algorithms for neighborhood variance and other moments

Edith Cohen, Haim Kaplan

January 2004 **Proceedings of the fifteenth annual ACM-SIAM symposium on Discrete algorithms**

Full text available:  [pdf\(305.61 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

The *neighborhood variance* problem is as follows. Given a (directed or undirected) graph with values associated with each node, compute a data structure that for any given node v and $r \geq 0$, would quickly produce an estimate of the variance of all values of nodes that lie within distance r from v . The problem can be generalized to other moment functions and to arbitrary distance-dependent decay. These problems are motivated by applications where the relevance of ...



6 Characterizations of flowchartable recursions short version

S. A. Walker, H. R. Strong

May 1972 **Proceedings of the fourth annual ACM symposium on Theory of computing**

Full text available:  [pdf\(771.41 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


In this paper we give new characterizations for the flowchartability of recursive functionals. The general question of flowchartability is recursively undecidable. We present here an effective map from recursions to representatives for which the question is decidable. The decision provides a good approximation to a characterization for general flowchartability in the following senses: (1) if a representative is flowchartable then the recursions it represents are, and (2) there is a straight ...



7 A Fast Optimal Robust Path Delay Fault Testable Adder

Bernd Becker, Rolf Drechsler, Rolf Krieger, Sudhakar M. Reddy

March 1996 **Proceedings of the 1996 European conference on Design and Test**

Full text available:  [pdf\(749.08 KB\)](#)

Additional Information: [full citation](#), [abstract](#)



[Publisher Site](#)

In this paper we explore the test complexity of the adder function with respect to the robust path delay fault model. A lower bound of $\Omega(n^2)$ for the cardinality of a complete test set for a combinational n -bit adder is proven. This result is valid for any adder design known until now. In addition we present a fast $O(\sqrt{n})$ -time adder that is fully robust path delay fault testable with a test set of size $\Theta(n^2)$.

Keywords: test, adder, path delay fault model



8 Session 6B: On the fractal behavior of TCP

Anna Gilbert, Howard Karloff

June 2003 **Proceedings of the thirty-fifth annual ACM symposium on Theory of computing**



Full text available:  pdf(355.07 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We propose a natural, mathematically tractable model of TCP which captures both its additive-increase, multiplicative-decrease behavior and its feedback mechanism. Neither a fluid nor a mean-field model, our model does not explicitly model the loss process; the losses are entirely determined by the rates of the sources at the time of buffer overflow. The system involves two sources competing to send packets into one recipient buffer of size B , from which bytes are drained at the rate of $< \dots$

Keywords: Internet, TCP, algorithm, fractal, network protocol

9 Task migration in hypercube multiprocessors

M.-S. Chen, K. G. Shin

April 1989 **ACM SIGARCH Computer Architecture News , Proceedings of the 16th annual international symposium on Computer architecture**, Volume 17 Issue 3


Full text available:  pdf(720.40 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Allocation and deallocation of subcubes usually result in a fragmented hypercube where even if a sufficient number of hypercube nodes are available, they do not form a subcube large enough to execute an incoming task. As the fragmentation in conventional memory allocation can be handled by memory compaction, the fragmentation problem in a hypercube can be solved by task migration, i.e., relocating tasks within the hypercube to remove the fragmentation. The procedure for tas ...

10 Embedding triple-modular redundancy into a hypercube architecture

D. I. Kiskis, K. G. Shin

January 1988 **Proceedings of the third conference on Hypercube concurrent computers and applications: Architecture, software, computer systems, and general issues - Volume 1**


Full text available:  pdf(827.13 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper describes an embedding of Triple Modular Redundancy (TMR) into a binary hypercube. The goal is to improve fault tolerance by masking any single-point faults. Each module of an application task is triplicated and executed in parallel on three nodes of a 2-dimensional subcube (Q_2) of the hypercube. Each of these nodes also executes a voter process. The remaining node is used for message passing only. All outputs from the triplicated modules are ...

11 Optimal algorithms for approximate clustering

Tomás Feder, Daniel Greene

January 1988 **Proceedings of the twentieth annual ACM symposium on Theory of computing**

Full text available:  pdf(1.08 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

In a clustering problem, the aim is to partition a given set of n points in d -dimensional space into k groups, called clusters, so that points within each cluster are near each other. Two objective functions frequently used to measure the performance of a clustering algorithm are, for any L_4 metric, (a) the maximum distance between pairs of points in the same cluster, and (b) the maximum distance between ...

12 Investigations of fault-tolerant networks of computers

Piotr Berman, J'anos Simon

January 1988 **Proceedings of the twentieth annual ACM symposium on Theory of computing**

Full text available:  pdf(923.48 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

13 On distributed processability of datalog queries by decomposing databases

Guozhu Dong

June 1989 **ACM SIGMOD Record , Proceedings of the 1989 ACM SIGMOD international conference on Management of data**, Volume 18 Issue 2Full text available:  pdf(1.03 MB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We consider distributed or parallel processing of datalog queries. We address this issue by decomposing databases into a number of subdatabases such that the computation of a program on a database can be achieved by unioning its independent evaluations on the subdatabases. More specifically, we identify two kinds of distributed-processible programs according to the properties of database decomposition. (i) A program is disjoint distributive if it is distrib ...

14 Session 9A: Dynamic generators of topologically embedded graphs

David Eppstein

January 2003 **Proceedings of the fourteenth annual ACM-SIAM symposium on Discrete algorithms**Full text available:  pdf(1.04 MB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We provide a data structure for maintaining an embedding of a graph on a surface (represented combinatorially by a permutation of edges around each vertex) and computing generators of the fundamental group of the surface, in amortized time $O(\log n + \log g(\log \log g)^3)$ per update on a surface of genus g ; we can also test orientability of the surface in the same time, and maintain the minimum and maximum spanning tree of the graph in time $O(\log \dots)$

15 Efficient handoff rerouting algorithms: a competitive on-line algorithmic approach

Yigal Bejerano, Israel Cidon, Joseph (Seffi) Naor

December 2002 **IEEE/ACM Transactions on Networking (TON)**, Volume 10 Issue 6Full text available:  pdf(785.97 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper considers the design of handoff rerouting algorithms for reducing the overall session cost in personal communication systems (PCS). Most modern communication systems that are used as an infrastructure for PCS networks are based on connection-based technologies. In these systems, the session cost is composed of two components. The setup cost represents the cost associated with the handoff operations, and the hold cost determines the expense related to the use of network resources held ...

Keywords: competitive analysis, connection management, handoff rerouting algorithms, online algorithms, personal communication systems (PCS)

16 Minimum-energy broadcasting in static ad hoc wireless networks

P. J. Wan, G. Călinescu, X. Y. Li, O. Frieder

November 2002 **Wireless Networks**, Volume 8 Issue 6Full text available:  pdf(254.58 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Energy conservation is a critical issue in ad hoc wireless networks for node and network life, as the nodes are powered by batteries only. One major approach for energy conservation is to route a communication session along the route which requires the lowest total energy consumption. This optimization problem is referred to as Minimum-Energy Routing. While the minimum-energy unicast routing problem can be solved in polynomial time by shortest-path algorithms, it remains open whether the minimum ...

Keywords: ad hoc wireless networks, energy conservation

17**Session 11A: Time-space tradeoffs, multiparty communication complexity, and**

nearest-neighbor problems

Paul Beame, Erik Vee

May 2002 **Proceedings of the thirty-fourth annual ACM symposium on Theory of computing**Full text available:  pdf(283.96 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

(MATH) We extend recent techniques for time-space tradeoff lower bounds using multiparty communication complexity ideas. Using these arguments, for inputs from large domains we prove larger tradeoff lower bounds than previously known for general branching programs, yielding time lower bounds of the form $T = \Omega(n \log^2 n)$ when space $S = n^{1-\epsilon}$, up from $T = \Omega(n \log n)$ for the best previous results. We also prove the first unrestricted separation of the power of general and oblivious ...

18 The complexity of searching a graph

N. Megiddo, S. L. Hakimi, M. R. Garey, D. S. Johnson, C. H. Papadimitriou

January 1988 **Journal of the ACM (JACM)**, Volume 35 Issue 1Full text available:  pdf(2.28 MB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

T. Parsons originally proposed and studied the following pursuit-evasion problem on graphs: Members of a team of searchers traverse the edges of a graph G in pursuit of a fugitive, who moves along the edges of the graph with complete knowledge of the locations of the pursuers. What is the smallest number $s(G)$ of searchers that will suffice for guaranteeing capture of the fugitive? It is shown that determining whether $s(G) \leq k$ is NP-complete.

19 The all-geodesic furthest neighbor problem for simple polygons

S. Suri

October 1987 **Proceedings of the third annual symposium on Computational geometry**Full text available:  pdf(1.14 MB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present an $O(n \log n)$ time and $O(n)$ space algorithm for the following problem in a simple polygon P with n vertices: For each vertex u of P , find another vertex $v(u)$ that is furthest from u , where the distance between two points is measured by the length of the shortest internal path connecting them.

20 Shape sensitive geometric permutations

Yunhong Zhou, Subhash Suri

January 2001 **Proceedings of the twelfth annual ACM-SIAM symposium on Discrete algorithms**Full text available:  pdf(711.03 KB)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We prove that a set of n unit balls in R^d admits at most $4d$ distinct geometric permutations, or line transversals, thus settling a long-standing conjecture in combinatorial geometry. The constant bound significantly improves upon the $O(n^{d-1})$ bound for the balls of arbitrary radii. Intrigued by this large gap between the two bounds, we also investigate how the number of geometric permutations varies as a function of shape, size, and dimension.

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[Google \(Web\)](#) [Yahoo!](#) [MSN](#) [CSB](#) [DBLP](#)**No documents match Boolean query. Trying non-Boolean relevance query.**500 documents found. Order: **relevance to query**.[Relative Expressiveness of Typed and Untyped Relational.. - Cabibbo, Van den Bussche \(Correct\) \(1 citation\)](#)With types, there are a finite number of pairwise **disjoint** domains, and each attribute has one of these into a finite number of domains, i.e. pairwise **disjoint** sets of values. We will denote the domains by a relation of some fixed type (we include a **proof** of this fact in this note) Our result shows that
151.100.16.20/pub/cabibbo/typed_algebra.ps[An Algorithm to Evaluate Quantified Boolean Formulae - Cadoli, Giovanardi, Schaerf \(1998\) \(Correct\) \(45 citations\)](#)a QBF in which the quantifiers are applied to k **disjoint** sets of variables and the sequence of
For example, if X_1, X_2 , and X_3 are mutually **disjoint** sets of propositional variables, then the
from $G(\Pi)$ by deleting all tautological clauses. **Proof.** Let us assume that $G_0(\Pi)$ is non-empty.
www.dis.uniroma1.it/pub/Al/papers/cado-giov-scha-98.ps.gz[Some New Results on Possibilistic Measurement - Cliff Joslyn \(1993\) \(Correct\)](#)is based on the observations of possibly non-**disjoint** intervals. From these set statistics an
derived random set $S \in \mathcal{E}$ If $F \in \mathcal{E}$ is a **disjoint** class, then $S \in \mathcal{E}$ generates a probability
to C (and monotone decreasing from C to 1. **Proof:** Let $x \in \mathbb{R}$. The **proof** will be carried out for x
ftp.c3.lanl.gov/pub/joslyn/nafigs93.ps.gz[Hausdorff dimension for horseshoes in \$\mathbb{R}^3\$ - Simon, Solomyak \(1998\) \(Correct\)](#)conditions $(H_1)H_3)H_1$ There exist **disjoint** closed intervals I_1, I_m whose union
'baker's maps' Let I_k be closed intervals with **disjoint** interiors such that $S_m(I_k) = I_k$
in the literature. Finally, a few comments on the **proof** of Theorem 1 are given. 1. Let F be a horseshoe
www.math.washington.edu/~solomyak/PREPRINTS/horse3.ps[Critical Motion Sequences and Conjugacy of Ambiguous Euclidean.. - Sturm \(1997\) \(Correct\) \(2 citations\)](#)even if there are discrete solutions or **disjoint** families of solutions. 1 Introduction 1.1
Euclidean reconstructions is partitioned into **disjoint** solution families or even discrete solutions.
transformation between any pair of P, P_i . **Proof.** Let the canonical projection matrices P, P_i be
www.inrialpes.fr/movi/people/Sturm/conjugacy.ps.gz[Specification and Proof in Membership Equational Logic - Bouhoula, Jouannaud, Meseguer \(1996\) \(Correct\) \(12 citations\)](#)now a K -kinded set $X = K_2 K X K$ of variables, whose **disjoint** subsets X_K for $K \in K$ are all denumerable
subsets X_K for $K \in K$ are all denumerable (and **disjoint** from Σ) we define the set of many-kinded
Specification and **Proof** in Membership Equational Logic Adel Bouhoula
www.loria.fr/~bouhoula/BouhoulaJM-CAAP97.ps[Coordinated Checkpointing-Rollback Error Recovery for.. - Janakiraman, Tamir \(1994\) \(Correct\) \(32 citations\)](#)partition the total virtual system space into **disjoint** task virtual spaces. Processes belonging to
Processes belonging to different tasks execute in **disjoint** address spaces and cannot share data. N Disk
ftp.cs.ucla.edu/tech-report/94-reports/940027.ps.Z[RPM: A Rapid Prototyping Engine For Multiprocessor.. - Barroso, Iman, Jeong.. \(1995\) \(Correct\) \(8 citations\)](#)may be shared (shared-memory systems) or **disjoint** (messagepassing systems) Within each class of
There are two dominant models. One is based on **disjoint** memories and message-passing and the other is
usc.edu/pub/CENG/1994/CENG-94-00.ps.Z[The 2-Hop Spanning Tree Problem. - Dahl \(1997\) \(Correct\) \(3 citations\)](#)If D is a directed graph and S and T are **disjoint** subsets of nodes in D the (S, T) denotes the
covering V such that these stars are pairwise **disjoint** except that they all contain the root r . We
optimal 2-hop spanning tree in the 2HST problem. **Proof.** Let y be a vertex of P . Thus, y is a feasible
www.ifi.uio.no/~ftp/publications/research-reports/GDahl-3.ps[Computing All Small Cuts in an Undirected Network - Nagamochi, Nishimura, Ibaraki \(1994\) \(Correct\)](#)

(9 citations)

while \vee means \vee For two **disjoint** subsets $XY \vee$ of a network $N, E(XY \vee N)$
 $6 = X \vee 6 = V$. We say that a cut X separates two **disjoint** subsets Y and $Y \vee 0$ of V if $Y \vee X$ and $Y \vee 0 \vee V$
 cut T can be computed in $O(n(m \vee n \log n))$ time. **Proof.** Assume $n \geq 3$. We use the $O(m \vee n \log n)$ time
www.kuamp.kyoto-u.ac.jp/labs/or/members/naga/paper/siamdm.ps.gz

Complete Enumeration of Equilibria for Two-Person Games in.. - Audet Hansen (Correct)

the union of a finite number of not necessarily **disjoint** polytopes called Nash subsets (Millham 1974)
 (1978) approaches the question through the **disjoint** bilinear formulation proposed by Mills (1960)
ftp.hpc.uh.edu/pub/ipco98/audet.ps

A Term Calculus for Intuitionistic Linear Logic - Benton, Bierman, de Paiva.. (1993) (Correct) (43 citations)

the assumptions of the two upper deductions are **disjoint**, i.e. their set of labels are **disjoint**. This
 are **disjoint**, i.e. their set of labels are **disjoint**. This upholds the fundamental feature of linear
 for both the sequent calculus and natural deduction **proof** systems. Our system differs from previous calculi
sunsite.doc.ic.ac.uk/public/packages/doc.ic.ac.uk/theory/guests/BiermanGM/tlca93.ps.gz

Simplifying the Development of Fault-Tolerant Distributed.. - Melliar-Smith And (Correct)

other. We have two problems. If the actions are **disjoint**, large parts of the state may need to be
 we discuss this below. If the actions are not **disjoint**, there is a more serious problem of reconciling
www.beta.ece.ucsb.edu/ftp/TOTEM/pdpip95.ps.Z

Logic with Probabilistic Operators - Alechina (1994) (Correct)

$2 \vee \Theta w$ and $\vee \Theta w$ is closed with respect to **disjoint** union. **Proof** sketch. Consistency is obvious. For
 $P \vee 1 (2 w)$ and is closed with respect to **disjoint** unions of definable subsets (due to $A2$)
 complete with respect to the class of PPL-models. **Proof** can be found in section 4. $2 \vee$ Claim $2 \vee$ Weak PPL is
www.illc.uva.nl/Publications/Lists/.../ResearchReports/CT-1994-11.text.ps.gz

Using Blocks for Constraint Satisfaction - Seybold, Metzger, Ogan, Simon (Correct)

vertices are adjacent. Two paths are vertex **disjoint** if they do not share any vertices except the
 are biconnected if there are at least two vertex **disjoint** paths between them. A block B (biconnected
 is preserved during block-consistency enforcement. **Proof** Block-consistency enforcement results in
ftp.inf.ethz.ch/pub/publications/tech-reports/2xx/297.ps.gz

Study of Scalable Declustering Algorithms for Parallel Grid Files - Bongki Moon (1996) (Correct) (7 citations)

to a single application. It divides the data into **disjoint** subspaces based on the processing requirements
 dense data. Grid files partition the dataset into **disjoint** subspaces and maintain a grid-based index
 if $M \vee 1 \vee 6 = 0 \vee 1 \vee M(1 \vee \Gamma 1 = \vee 1) \vee 1 \vee M \vee ?$: **Proof.** Given in [19] Theorem 1 gives closed form
ftp.cs.umd.edu/pub/papers/papers/ncstrl.umcp/CS-TR-3589/CS-TR-3589.ps.Z

Towards an Ontology for Library Modalities - Welty (1998) (Correct) (1 citation)

is a document. $3 \vee$ Ontology There are two major (**disjoint**) modality types: internal-modality and
 as postscript and PDF) The purpose of the latter **disjoint** categories is to distinguish text that can
www.cs.vassar.edu/~weltyc/papers/multi-modal98.ps

Exploiting Signal Flow and Logic Dependency in Standard Cell.. - Cong, Xu (1995) (Correct) (1 citation)

then MFFC $w \vee$ MFFC $v \vee P2$) Two MFFCs are either **disjoint** or one must contain another. Based these
 decompose a combinational circuit N into a set of **disjoint** MFFCs as follows: i) choose a primary output
ballade.cs.ucla.edu:8080/~cong/papers/aspdac95.ps.Z

A Hybrid Multilevel/Genetic Approach for Circuit Partitioning - Alpert, Hagen, Kahng (1996) (Correct)(9 citations)

one. A bipartitioning $P = fXY \vee g$ is a pair of **disjoint** clusters (i.e. subsets of V) X and Y such
 A k -way clustering of $H(\vee E)$ is a set of **disjoint** clusters $P \vee k \vee C1 \vee C2 \vee Ck$ such that
visicad.cs.ucla.edu/papers/conference/c54.ps

A Problem with Long-Term Computing Processes, and What Can Be ... - Naftaly Minsky (Correct)

to be auditable, a system S must consist of two **disjoint** parts: the base-part $S \vee b$, whose purpose is to
 maintenance of S should be partitioned into two **disjoint** groups: the group $P \vee b$ responsible for the base
www.cs.rutgers.edu/pub/technical-reports/lcsr-tr-226.ps.Z

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multiple path backtracking

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Results 1 - 10 of about 3,960 for multiple path backtracking. (0.08 seconds)

Backtracking in a Multiple-Window Hypertext Environment

M Bieber, J Wan - ECHT, 1994 - portal.acm.org

... node might belong to **multiple** logical tasks (such as window B in Figure 1, and nodes c and in Figure 3). If a **multi-path** node is removed upon **backtracking**, we ...

Cited by 26 - Web Search - portal.acm.org - ecs.soton.ac.uk

Logic systems for path delay test generation.

S Bose, P Agrawal, VD Agrawal - IEEE, IEEE SERVICE CENTER, PISCATAWAY, NJ(USA), 1993,, 1993 - ieeexplore.ieee.org

... 23-value system to test generation for **multiple path** faults in ... steady at 0). Consider the off-**path** input of ... This may lead to unnecessary **backtracking** dur- ing ...

Cited by 13 - Web Search - ieeexplore.ieee.org - csa.com

Multiple path sensitization for hierarchical circuit testing

CC Su, C Kime - Proc. International Test Conference, 1990 - ieeexplore.ieee.org

Page 1. **Multiple Path** Sensitization for Hierarchical Circuit Testing ... The Interface for HPath HPath is a versatile **multiple path** sensitization algo- rithm. ...

Cited by 3 - Web Search - ieeexplore.ieee.org

On path delay testing in a standard scan environment

P Varma, CCT Inc, CA San Jose - The 1994 IEEE International Test Conference, 1994 - ieeexplore.ieee.org

... 27] to generate two system clock cycle functional justification based **path** delay tests ... sion techniques to take advantage of clock gating or **multiple** clocks in ...

Cited by 13 - Web Search - ieeexplore.ieee.org - portal.acm.org - csa.com

Speed Up of Test Generation Using High-Level Primitives

RP Kunda, JA Abraham, BD Rathi, P Narain - DAC, 1990 - portal.acm.org

... Backtrack if there is a conflict. If the primitive has **multiple** fault-free/faulty outputs, then select a **path** for fault ... DEPENDENCY DIRECTED BACKTRACKING ...

Cited by 18 - Web Search - ieeexplore.ieee.org - portal.acm.org - ieeexplore.ieee.org

A high-level approach to test generation

... , J Propagation, E Implication, DD **Backtracking** - IEEE Transactions on Circuits and Systems I Fundamental ... , 1993 - ieeexplore.ieee.org... A detailed description of the dependency-directed **backtracking** scheme is provided in ... This simplified the process of justified and **multiple path** propagation. ...

Cited by 3 - Web Search - ieeexplore.ieee.org

DYNAMITE: An Efficient Automatic Test Pattern Generation System for Path Delay Faults

K Fuchs, F Fink, MH Schulz - IEEE Trans. Computer-Aided Design, 1991 - ieeexplore.ieee.org

... at a. Actually, **path** P, can easily be shown to be not testable under the restrictive condition of robustness that does justice to a **multiple** fault assumption ...

Cited by 72 - Web Search - ieeexplore.ieee.org - csa.com

MUST: multiple-stem analysis for identifying sequentially untestable faults

Q Peng, M Abramovici, J Savir, L Technologies - ITC, 2000 - ieeexplore.ieee.org

... 3 **Multiple** Stem Analysis Algo- rithm ... a=b=l disables the unique propagation **path** through gate ... of these conditions are deter- mined by **backtracking**-based search. ...

Cited by 10 - Web Search - ieeexplore.ieee.org - portal.acm.org - portal.acm.org

MP-DSR: A QoS-Aware Multi-Path Dynamic Source Routing Protocol for Wireless Ad-Hoc Networks

R Leung, J Liu, E Poon, ALC Chan, B Li - CONF LOCAL COMPUT NETWORKS. pp. 132-141. 2001, 2001 - doi.ieeecs.org

... Disjoint Paths. In **multiple path** routing, data transmission fails if and only if all disjoint paths fail at the same time. Thus, the ...

Cited by 18 - [Web Search](#) - [doi.ieeecomputersociety.org](#) - [eecg.toronto.edu](#) - [tc-net.insa-lyon.fr](#) - [all 9 versions »](#)

[PS] A parallel Prolog resolution based on multiple unifications

I Vlahavas, P Kefalas - Parallel Computing, 1992 - [csd.auth.gr](#)

... **backtracking**, new resolvents are placed at the top of a stack. In every resolution step the ... 3. **Multiple Path** Parallel Unification (MPU) Resolution ...

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multiple path backtracking

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multiple path hamming

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Liver fibrosis after extracorporeal shock-wave lithotripsy of gallbladder stones

PW Plaisier, JF Hamming, RL Hul, R Toom, HA ... - Surgical Endoscopy, 1994 - springerlink.com

... P. W. Plaisier, J. F. Hamming, R. L. van ... to cholecystectomy the patient underwent

multiple extracorporeal shock ... to the shock-wave transmission **path**, which was ...

Cited by 1 - Web Search - ncbi.nlm.nih.gov

Information and cut-off rates for multiple-pulse position modulation on multi-path channels

H Park - IEE PROC OPTOELECTRON, 2000 - ieeexplore.ieee.org

... we can expect the received signal with **multiple** reflections to ... A natural model for the multi-**path** channel is ... have a unity weight and equal **Hamming** distance of 2 ...

Cited by 1 - Web Search - ieeexplore.ieee.org - link.aip.org - csa.com

Design of an optimal test pattern generator for built-in self testing of path delay faults

DK Das, I Chaudhuri, BB Bhattacharya - Proceedings of the Eleventh International Conference on VLSI ..., 1998 - ieeexplore.ieee.org

... s), such a pair is known as single (**multiple**)-input-change ... In SIC test scheme of **path** delay fault testjug, a ... that differs from X by unit **hamming** distance in a ...

Cited by 4 - Web Search - ieeexplore.ieee.org - portal.acm.org - portal.acm.org

Design of an Augmented Generalized Cube Network

BS Lee, DH Chi, TD Han - ICPADS, 1997 - doi.ieeecomputersociety.org

... ($0 < j \leq h$, A_k, A_l : two destination nodes whose **Hamming** distance is h) N. ... istics of AGCN 3.1 Performance analysis of AGCN Generally, a **multiple path** MIN shows ...

Web Search - doi.ieeeecs.org - ieeexplore.ieee.org - portal.acm.org - all 5 versions »

Hybrid DS/SFH Spread-Spectrum Multiple Access with Predetection Diversity and Coding for Indoor ...

J Wang, M Moeneclaey - IEEE Journal on Selected Areas in Communications, 1992 - ieeexplore.ieee.org

... conjunction with interleaved channel coding [**Hamming** (7, 4 ... the frequency of the reference **path** signal. ... MOENECLAAY: HYBRID DSISFH SPREAD-SPECTRUM **MULTIPLE ACCESS** ...

Cited by 8 - Web Search - ieeexplore.ieee.org - csa.com

An extension of path coupling and its application to the Glauber dynamics for graph colorings

M Dyer, LA Goldberg, C Greenhill, M Jerrum, M ... - PROC ANNU ACM SIAM SYMP DISCRETE ALGORITHMS, 2000 - epubs.siam.org

... We run the **path** coupling for **multiple** steps and use the expected behavior of the coupling at a certain stopping time to bound the expected behavior of the ...

Cited by 15 - Web Search - comp.leeds.ac.uk - eecs.harvard.edu - dcs.ed.ac.uk - all 17 versions »

Multiuser detection for multi-rate CDMA in multi-path fading channels

PW Fu, KC Chen - IEEE VEH TECHNOL CONF, 2000 - ieeexplore.ieee.org

... where the high data rate is an integer **multiple** M of ... of the kth generalized user in multi-**path** fading channels ... where $4s$) is the **Hamming** weight of E, A_i is the ...

Web Search - ieeexplore.ieee.org - csa.com

... detectors with decision feedback for asynchronous spread-spectrum multiple access in multipath ...

BC Skelton, DP Taylor - IEEE Transactions on Communications, 2001 - ieeexplore.ieee.org

... receiver family in an -**path** multipath fading ... 3]. The technique employed treats the **multiple**-access interference ... noise (AWGN), is the **Hamming** distance between ...

Web Search - ieeexplore.ieee.org - emis.de - csa.com

Design and Characterization of an Error-Correcting Code for the SONET STS-1 TributaryAWH Codes - IEEE TRANSACTIONS ON COMMUNICATIONS, 1990 - ieeexplore.ieee.org... The simple **Hamming** code¹ seems well suited because we ... and do not assume any exotic**multiple**-error environment ... one tributary, 2) additive signal-**path** delay must ...Cited by 6 - [Web Search](#) - ieeexplore.ieee.org - csa.comConclusions: We have demonstrated a method of fault local-WF **Hamming** - Appl. Opt, 1976 - ieeexplore.ieee.org... **Hamming** WF: ... 3 **Path** of travelling of input signal in MIT ... The effect of **multiple** reflections between taps is fully allowed for, and almost arbitrary phase and ...[Web Search](#)

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Multiple Trace Theory of Human Memory: Computational, Neuroimaging, and Neuropsychological Results

A Tucson - HIPPOCAMPUS, 2000 - doi.wiley.com

... 100%. The criterion for a successful recall was a **Hamming** distance of less than20. ... **MULTIPLE TRACE THEORY OF HUMAN MEMORY** 357 Page 7. strate ...Cited by 78 - [Web Search](#) - [merlin.psych.arizona.edu](#) - [hmb.utoronto.ca](#) - [psychol.ucl.ac.uk](#) - [all 6 versions](#) »

Quadratic forms, generalized **Hamming** weights of codes and curves with many points

G van der Geer, M van der Vlugt - J. Number Theory, 1996 - arxiv.org

... In a series of papers [GV-2, 3, 4] we showed that there is a relation between generalized **Hamming** weights of **trace** codes and the number of rational points on ...Cited by 11 - [View as HTML](#) - [Web Search](#) - [ingentaconnect.com](#) - [adsabs.harvard.edu](#) - [all 6 versions](#) »

[PS] SpotLight: Best-First Search of FSM State Space

CH Yang, DL Dill - IEEE International High Level Design Validation and Test ..., 1996 - sprout.stanford.edu

... **Hamming** Distance to Error Zone N **Multiple** Checks N **Hamming** distance ... checks N Uses**Hamming** distance to do Best-First Search ... 33 Error **Trace** inside the Error Zone ...Cited by 4 - [View as HTML](#) - [Web Search](#) - [verify.stanford.edu](#)

Quasi-orthogonal sequences for code-division **multiple**-access systems

K Yang, YK Kim, PV Kumar - IEEE Trans. Inform. Theory, 2000 - ieeexplore.ieee.org

... of channels in synchronous code-division **multiple**-access (CDMA ... of with , where denotes the **Hamming** distance between ... The **trace** function is a mapping from to , ...Cited by 19 - [Web Search](#) - [ece.iisc.ernet.in](#)

Wavelength division **multiple** access channel hypercube processor interconnection

PW Dowd - IEEE Transactions on Computers, 1992 - doi.ieeecomputersociety.org

... Diameter: A node $P = (p_1, \dots, p_l)$ has one port for each incident **multiple** access channels ... the degree is T. The distance between nodes is the **Hamming** distance be ...Cited by 58 - [Web Search](#) - [doi.ieeeecs.org](#) - [portal.acm.org](#) - [ieeexplore.ieee.org](#) - [all 7 versions](#) »

Instruction execution sequence confirmation

KP Litchfield - ACM SIGARCH Computer Architecture News, 1994 - portal.acm.org

... analysis of an instruction execution **trace** is not ... it becomes extremely unlikely that **multiple** malfunctions will ... a minimal code would have **Hamming** distance 4. A ...[Web Search](#) - [portal.acm.org](#)

Multiple region MRI

SK Nagle, DN Levin - Magnetic Resonance in Medicine, 1999 - doi.wiley.com

... **Multiple** Region MRI ... cells that support I, $I(x, y)$ is the reconstructed noise image, and each coarse k-space grid has the dimensions $N \times N \times y$. The **trace** on the ...Cited by 12 - [Web Search](#) - [naglenowlan.net](#) - [www-radiology.uchicago.edu](#) - [ncbi.nlm.nih.gov](#) - [all 6 versions](#) »

Multiple-phase codes for detection without carrier phase reference

FW Sun, H Leib - IEEE Trans. Inform. Theory, 1998 - ieeexplore.ieee.org

... **Hamming** codes. Many of these new codes provide significantly larger coding gains than previously known codes. Linear codes derived from Code- Division **Multiple**- ...Cited by 8 - [Web Search](#) - [ieeexplore.ieee.org](#)

Generalized **multiple** description coding with correlating transforms

VK Goyal, J Kovacevic - IEEE Transactions on Information Theory, 2001 - ieeexplore.ieee.org

... GOYAL AND KOVACEVIC: GENERALIZED **MULTIPLE** DESCRIPTION CODING WITH CORRELATING

TRANSFORMS ... Witsenhausen, Wolf, and Ziv for a binary source and **Hamming** distortion ...
Cited by 56 - [Web Search](#) - [andrew.cmu.edu](#) - [rle.mit.edu](#) - [lcavwww.epfl.ch](#) - [all 6 versions »](#)

... GF (q). that is a basis $\{o. ri... n\}$ verifies $Tr_5(Q'to) = h,)$ Trq is the **trace** func! ion of ...
I INTRODUCTION - IEEE TRANSACTIONS ON INFORMATION THEORY, 1995 - [ieeexplore.ieee.org](#)
... $r = -11/2$], codewords in $RM_{r,n}$ have **Hamming** weight **multiple** of 2, for ttt cvcn,
and **multiple** of 4 ... $Tr_5(Q'to) = h,)$ Trq is the **trace** function ...
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multiple trace hamming model

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Multiple Trace Theory of Human Memory: Computational, Neuroimaging, and Neuropsychological Results

A Tucson - HIPPOCAMPUS, 2000 - doi.wiley.com

... criterion for a successful recall was a **Hamming** distance of ... i) the normal forgetting in the connectionist **model** is higher ... **MULTIPLE TRACE THEORY OF HUMAN MEMORY** ...

Cited by 78 - [Web Search](#) - [merlin.psych.arizona.edu](#) - [hmb.utoronto.ca](#) - [psychol.ucl.ac.uk](#) - [all 6 versions](#) »

Series solution for two-frequency Bragg interaction using the Korpel-Poon multiple-scattering model

RK Appel, MG Somekh - Journal of the Optical Society of America A: Optics and ..., 1993 - josaa.osa.org

... using the Korpel-Poon **multiple-scattering model** R. K. Appel and M. G. Somekh ... Section

2 presents an overview of this Korpel-Poon **multiple-scattering model**. ...

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[PS] SpotLight: Best-First Search of FSM State Space

CH Yang, DL Dill - IEEE International High Level Design Validation and Test ..., 1996 - sprout.stanford.edu

... **Hamming** Distance to Error Zone N **Multiple** Checks N **Hamming** distance ... checks N Uses

Hamming distance to do Best-First Search ... 33 Error **Trace** inside the Error Zone ...

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... neurons in the normal and the tremulous 1-methyl-4-phenyl-1, 2, 3, 6-tetrahydropyridine vervet model ...

A Raz, E Vaadia, H Bergman - J Neurosci, 2000 - jneurosci.org

... All of the cross-correlograms have **multiple** significant peaks ... is the spike train,

the middle **trace** is the spike train smoothed with a digital **Hamming** window 20 ...

Cited by 68 - [Web Search](#) - [basalganglia.huji.ac.il](#) - [motorcortex.huji.ac.il](#) - [ncbi.nlm.nih.gov](#) - [all 5 versions](#) »

Dynamics and statistical mechanics of the Hopfield model

AD Bruce, EJ Gardner, DJ Wallace - Journal of Physics A: Mathematical and General, 1987 - iop.org

... the distribution p of the fractional **Hamming** distance D ... capacity is obtained for

the **S model** ($A = 1$... Then, after doing the **trace** over the random variables $V:r$...

Cited by 13 - [Web Search](#) - [iop.org](#) - [adsabs.harvard.edu](#)

Dynamic publication model for neurophysiology databases

D Gardner, M Abato, KH Knuth, R DeBellis, SM Erde - Philos Trans R Soc Lond B Biol Sci, 2001 - journals.royalsoc.ac.uk

... Acknowledging **Hamming's** (1962) dictum 'the purpose of computing is ... __ series __ view

can include **multiple** traces from any or all subtypes of time __ **trace**. ...

Cited by 12 - [Web Search](#) - [ncbi.nlm.nih.gov](#)

Linear dynamic model for design of stable explicit-rate ABR control schemes

Y Zhao, SQ Li, S Sigarto, TRI Southwestern Bell, ... - PROC IEEE INFOCOM, 1997 - doi.ieeecs.org

... With the first **model** of $W(z) = 5$, the Ha ... I 1 for MPEG video traffic especially when

multiple video connections are ... like a sin- gle MPEG video **trace**, the scheme ...

Cited by 39 - [Web Search](#) - [doi.ieeecomputersociety.org](#) - [ece.utexas.edu](#) - [portal.acm.org](#) - [all 7 versions](#) »

Optimal geometric model matching under full 3 D perspective

JR Beveridge, EM Riseman - PROC IEEE CAD BASED VISION WORKSHOP, IEEE, LOS ALAMITOS, CA, ..., 1994 - ieeexplore.ieee.org

... Figure 5. The lower table shows a **trace** terminating with ... With all our local search

algorithm, **multiple** trials are ... of the n neighbors in the **Hamming**-Distance-1 ...

Cited by 34 - [Web Search](#) - [portal.acm.org](#) - [csa.com](#) - [all 5 versions](#) »

Using Second-Order Power Analysis to Attack DPA Resistant SoftwareTS Messerges - CHES, 2000 - springerlink.com... by a simple **model** based on **Hamming** weights. ... one or more samples within a single power **trace**. ... properties of the power consumption at **multiple** sample times ...Cited by 54 - [Web Search](#) - portal.acm.org - portal.acm.orgWavelength division **multiple** access channel hypercube processor interconnectionPW Dowd - IEEE Transactions on Computers, 1992 - doi.ieeecomputersociety.org... Diameter: A node $P = (p_1, \dots, p_l)$ has one port for each incident **multiple** access channels ... the degree is T . The distance between nodes is the **Hamming** distance be ...Cited by 58 - [Web Search](#) - doi.ieeecs.org - portal.acm.org - ieeexplore.ieee.org - [all 7 versions »](#)

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[Parallel and Distributed Search for Structure in...](#) - Oates, Schmill, Cohen (1996) (Correct) (3 citations)
 Distributing the search for structure over **multiple** processors or networked machines makes mining of
www-eksl.cs.umass.edu/papers/Oates96a.ps

[Reactive Visual Control of Multiple Non-Holonomic Robotic Agents](#) - Han, Veloso (1998) (Correct) (3 citations)
 Reactive Visual Control of **Multiple** Non-Holonomic Robotic Agents Kwun Han Manuela
 point is marked with a cross. Figure 5: Sample **trace** of the execution of the reactive control
 $= y \quad k \quad \Delta k \quad k_1 = k$ The above equations **model** the ball with simple Newtonian dynamics. k is a
www.cs.cmu.edu/afs/cs/user/kwunh/www/pubs/visual.ps.gz

[Supporting Transcontinental Collaborative Work in Persistent...](#) - Leigh, Johnson (1996) (Correct) (6 citations)
 create a persistent virtual environment enabling **multiple** transcontinentally-situated participants to
 the dominant perspective for viewing architectural **models** in VR is through an egocentric (inside-out)
 There is a large collection of VRML architectural **models** appearing on the Web which can be used as **models**
www.evl.uic.edu/aej/papers/ieeecga.ps.gz

[Improvements on a Heuristic Algorithm for Multiple-Query...](#) - Shim (1994) (Correct) (20 citations)
 Improvements on a Heuristic Algorithm for **Multiple-Query** Optimization Kyuseok Shim y Timos
 be extended to deal with implied relationships. To **model** the existence of both identical and implied
 implications among tasks are included in the cost **model**. The following quantities were used as input
dmlab.kaist.ac.kr/~shim/dke94.ps.gz

[A SPMD Monitor/Debugger for the Fujitsu Cellular Array...](#) - Johnson, Mackerras (1993) (Correct) (1 citation)
 of our debugger provides for this with **multiple** simultaneous views, with the ability to add
 The event history style of debugging uses a **trace** of the program's behaviour, recorded during a run
 such as fixed geometric mesh or worker-farm **models**, can be supported by debugging methods tailored
cs.anu.edu.au/people/Chris.Johnson/workshop_90.ps

[Walk Backwards to Happiness - Debugging by Time Travel](#) - Booth, Jones (1997) (Correct) (3 citations)
 for debugging that revolves around being able to **trace** back through the history of a particular
www.ep.liu.se/ea/cis/1997/009/14/cis9700914.ps

[Cache Optimization For Portable Computers](#) - Su (1995) (Correct)
 nowadays. It examines the variability between **multiple** runs of same program and the impact of
 the focus of much research [1, 2, 3, 8, 9] 1.2 **Trace-Driven Simulation** **Trace-Driven Simulation** is a
 are needed to obtain accurate results, a simulation **model** and input **trace** data which represents the memory
ccc.cs.byu.edu/publications/ps/jun_prop.ps.gz

[Trace-Driven Simulation of Data-Alignment and other Factors ...](#) - Markatos, Chronaki (1994) (Correct)
 memory access. Unfortunately, the existence of **multiple** copies of the same page introduces the problem
Trace-Driven Simulation of Data-Alignment and other
 appear in Proceedings of International Workshop on **Modeling, Analysis and Simulation of Computer and**
www.ics.forth.gr/proj/arch-vlsi/papers/1994.MASCOTS94.DATA_ALIGNMENT_IN_VIRTUAL_SHARED_MEMORY.ps.Z

[Design and Implementation of a Replay Framework based on a...](#) - Ihrig, Kambhampati (1996) (Correct)
 (7 citations)
 in solving multi-goal problems. Cases covering **multiple** goals are stored only when subplans for
 attempts a new problem, and achieves a solution, a **trace** of the decisions that fall on the derivation path
 it has an adequate theory of problem similarity (Hammond 1990 Ihrig 1996 Ihrig & Kambhampati 1994
enws318.eas.asu.edu/pub/rao/doc/ihrig-aaai96-sent.ps

[Visualizing and Querying Distributed Event Traces with Hy+](#) - Consens, Hasan, Mendelzon (1994) (Correct)
 (2 citations)
 communication events ev(A node may have **multiple** blobs associated with it, and blobs may be

Visualizing and Querying Distributed Event **Traces** with Hy Mariano P. Consens, Masum Z. Hasan, Fig. 1. Visualizing tuples and hygraphs. data **model** we use is more general than graphs. Hygraphs, are ftp.cs.toronto.edu/db/papers/adb94.col.ps.Z

A Model for Secure Protocols and Their Compositions - Heintze, Tygar (1996) (Correct) (34 citations)
many times. It is therefore important to **model multiple** (and possibly interleaved) invocations of the in Figure 1. Figure 2 presents one possible **trace** of agent behavior. It records the actions of a
A Model for Secure Protocols and Their Compositions
www.cs.cmu.edu/afs/cs.cmu.edu/user/tygar/www/papers/timed-models/main.ps

Instruction Cache Effects of Different Code Reordering Algorithms - Lee (1994) (Correct) (4 citations)
demand on the instruction cache as code from **multiple** working sets need to coexist in the cache algorithms on the MIPS R2000 [Kane 88] using **trace** based simulations. We show that code reordering a reasonable sized first level cache. Therefore, we **modeled** a processor with a split first level cache. The www.cs.washington.edu/homes/dlee/mypapers/quals.ps

TCP Vegas: New Techniques for Congestion Detection and.. - Brakmo, O'Malley, Peterson (1994) (Correct) (293 citations)
network. Specifically, the simulator supports **multiple** hosts, each running a full protocol stack 180 240 300 360 420 480 540 600 Figure 1: TCP Reno **Trace** Examples. In this environment, actual x-kernel connections and ethernet)Routers can be **modeled** either as a network node running the actual IP netweb.usc.edu/yaxu/Vegas/Reference/vegas93.ps

Partial Orderings of Event Sets and Their.. - Luckham, Vera.. (1992) (Correct) (39 citations)
data type styles of defining interfaces, and **multiple** inheritance methods of reusing old interfaces to the poset computation **model** in place of linear **traces** of events (possibly with timestamps)and in design goals has been to adopt a computation **model** in which the synchronization, concurrency, theory.stanford.edu/pub/katiyar/papers/Rapide/jss93.ps

Architectures, Costs, and Transformations - Skillicorn (Correct)
easy to make. 4 Parallel Software Development for **Multiple** Target Architectures Parallel programs can be is executed, and gives rise to an implementation **trace**. Even for conventional imperative languages, a This paper explores the relationship between cost **models** and software development in a parallel setting. www.qucis.queensu.ca/home/skill//sweden.ps

A Real-Time Interval Logic and Its Decision Procedure - Ramakrishna, Dillon.. (1993) (Correct) (1 citation)
logic. Thus, a formula is interpreted on a linear **trace** of states, representing a possible execution of a [2, 6] but unlike [10, 14, 20, 24]uses a dense **model** of time. A dense time domain is preferable to a underlying time domain, as required for a discrete **model**. A dense time domain is also preferable for alpha.ece.ucsb.edu/ftp/RTGIL/fsttcs93.ps.Z

Performing Replay in an OSF DCE Environment - Yuh Ming (1995) (Correct) (1 citation)
possible causes. A distributed program may have **multiple** processes or threads running on different (or application to collect and display event **traces** showing the execution of its component processes the caller events, since in the client/server **model** of computation the caller events affect how the ccnga.uwaterloo.ca/pub/papers/Ps/conf11.ps.Z

EDPEPPS: An Environment for the Design and.. - Delaitre, Justo.. (1996) (Correct) (3 citations)
program behaviour. EDPEPPS is based on an MPMD (**Multiple** Process **Multiple** Data) process **model**, and views an animation-based representation of the resulting **trace**. The tools have been placed in many research can rely on a simple and stable programming **model** (the von Neumann machine)parallel programmers www.cpc.wmin.ac.uk/~edpepps/reports/edpepps18.ps.gz

On-line Avoidance of the Intrusive Effects of Monitoring on.. - Wanqing Wu (1996) (Correct) (1 citation)
distributed programming languages are composed of **multiple** processes which are assigned for execution to be performed off-line by postprocessing execution **traces** collected by running the instrumented program [5, section we first describe the distributed execution **model** assumed in this paper and then identify the www.cs.pitt.edu/~gupta/research/Dist/icdcs96a.ps

Labolmage: a Workstation Environment for Research in Image .. - Alain Jacot-Descombes (Correct)
also contains various specialized tools. These **multiple** processing modules and tools are first geometrical transform. During the entire session, a **trace** of all the operations appears in the master median and high pass filter, Gaussian and **Hamming** filter, edge preserving smoothing. cui.unige.ch/sgaico/research/geneve/vision/ps.Z/Jacot.9307.LabolImage.ps.Z

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[A Taste of Crispy Squid - Gadde, Chase, Rabinovich \(1998\) \(Correct\) \(21 citations\)](#)
controlled conditions. Proxycizer accommodates **multiple trace** formats, allows a choice of request timing
Latency of the internal network. As the network **distance** between the servers in a collective cache
an exclusive portion of the global directory. This **model** is well-suited to typical ISP environments in
www.cs.wisc.edu/~cao/WISP98/final-versions/chase.ps

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Searching for **multiple backtrace and distance and model**.

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[Multiple Shrinkage and Subset Selection in Wavelets - Clyde, Parmigiani, Vidakovic \(Correct\) \(43 citations\)](#)

Multiple Shrinkage and Subset Selection in Wavelets By elements. We do this using a Bayesian hierarchical **model**, assigning a probability distribution to the We focus on two important issues in statistical **modeling** with wavelets. The first is dimension ftp.isds.duke.edu/pub/Users/brani/papers/ModMixWav.ps

[Writing a Client-Server Application in C++ - Guedes, Julin \(1992\) \(Correct\) \(1 citation\)](#)

and implementation and on the support for **multiple** implementations of the same interface. The class Abstract Applications based on the client-server **model** place a special emphasis on the specification of issues into account. In this paper we present a **model** for writing client-server applications in C ftp.cs.cuhk.hk/pub/mach3/src/mach_us/src/doc/usenix-c++-92.ps

[Implementation Of The Conjugate Gradient Method Using Short.. - Barth \(1996\) \(Correct\)](#)

Of The Conjugate Gradient Method Using Short **Multiple** Recursions By Teri L. Barth B. S. Colorado www-math.cudenver.edu/graduate/thesis/tbarth.ps.gz

[Classifying Seismic Signals by Integrating Ensembles of.. - Shimshoni, Intrator \(1996\) \(Correct\) \(1 citation\)](#)

a classification scheme based on integration of **multiple** Ensembles of ANNs. It is demonstrated on a local events vs. conventional explosions at near **distances**. Many of the methods appear in the literature of a-priori knowledge about the true underlying **model** of the data in many real-life problems, leads the ftp.math.tau.ac.il/pub/nin/papers/hong-kong.ps.Z

[Simulation Based Performance Evaluation of Mobile, Ad.. - Das, Castañeda, ... \(2000\) \(Correct\) \(9 citations\)](#)

routing algorithm or TORA [28] is maintaining **multiple** routes to the destination so that many traditional protocols, such as link state and **distance** vector, used for dynamic networks. Performance and routing load for a given traffic and mobility **model**. Both small (30 nodes) and medium sized (60 ringer.cs.utsa.edu/~jyan/paper/journal.ps

[Looking Forward: Education Network At The Threshold Of The.. - Castañeda \(Correct\)](#)

collaboration and cooperation links to develop **distance** education, within a scope either international, we concluded as follows: 1. Any already made **model** was not a custom made **model** for us. 2. We had to 1. Any already made **model** was not a custom made **model** for us. 2. We had to design our own **model** in cread.cstudies.ubc.ca/pdf_files/CREADProceedings/MorenoEngl.PDF

[The Primitive Distance-Transitive Representations of ... - Linton, Lux, al. \(1994\) \(Correct\)](#)

one of the subdegrees would have to be a proper **multiple** of the bound, and the unexhibited orbit, which The primitive **distance**-transitive representations of $F_i 22$, $F_i 22$ tamdhu.dcs.st-and.ac.uk/pub/sal/fispap/fispap.ps.gz

[Comparative Performance Evaluation of Routing.. - Das.. \(1998\) \(Correct\) \(41 citations\)](#)

routing algorithm or TORA [20] is maintaining **multiple** routes to the destination so that many traditional protocols, such as link state and **distance** vector, used for dynamic networks. Performance and routing load for a given traffic and mobility **model**. It is observed that the new generation of ringer.cs.utsa.edu/~jyan/paper/IC3N.ps

[Fuzzy Behavioral Mobile Robot Navigation: Case Studies - Tong Paul \(Correct\)](#)

sensory systems. For example, to describe the **distance** of an obstacle in front of the robot, a robot. Basic behavior of a mobile robot can be **modeled** by a set of fuzzy rules. Through fuzzy systems are proposed to overcome complexity of **modeling** and uncertainty of internal representation of www.ai.polymtl.ca/wbtong/papers/jcis98ic_2.ps.gz

[A courseware development methodology for Open and Distance Learning - Retalis \(Correct\)](#)

A courseware development methodology for Open and **Distance** Learning S. Retalis (retal@softlab.ntua.gr) Na design and development (IDD) approaches and **models** like Romizowski's, Reigeluth's, Gagne's, etc. based on variations of the waterfall life cycle **model**. The V-life cycle **model** has in its turn offered a

www.uni-koblenz.de/fb4/publikationen/gelbereihe/RR-14-97/retalis.ps.gz

A Framework For Collision Detection And Response - Lennerz, Schömer, Warken (Correct) (3 citations)
of our algorithms for collision detection and **distance** calculation. Both are described in section 2.3 and **distance** calculations for polygonal **models**. The simplest method to detect collisions
For reasons of simplicity virtual objects are often **modelled** as rigid bodies, which are not subject to any
www-hotz.cs.uni-sb.de/~schoemer/publications/ESS99.ps.gz

Multi-Level Colour Halftoning Algorithms - Ostromoukhov, Emmel, Rudaz.. (Correct)
dot size ink-jet printers able to reproduce **multiple** intensity levels. Multi-level inkjet printers
output colour is made by computing the euclidian **distance** between the desired output colour plus the error
diwww.epfl.ch/w3lsp/pub/papers/colour/mlcha.ps.gz

Parallelizing Applications into Smart Memories - Babb, Rinard, Moritz, Lee.. (Correct)
we use for decomposing the application data into **multiple** clusters of small memories. Next, Section 5
locality and minimizes physical communication **distance**. It also generates an efficient static schedule
the interconnection between these memories. In our **model**, computation is performed in custom logic
www.cag.lcs.mit.edu/~barua/papers/smart-mem.ps.Z

On a Multi-Objective Evolutionary Algorithm and Its.. - Günter Rudolph (1998) (Correct) (21 citations)
the decision maker is almost always faced with **multiple**-usually conflicting-goals. In this
will be considerably simplified. For example, the **distance** $d(x \times \min_k x \backslash \Gamma_{\max} k : x \times 2 \times X$
ls11-www.informatik.uni-dortmund.de/people/rudolph/papers/icec98.ps.gz

Camera Viewpoint Control for the Automatic Reconstruction of.. - Niem, Steinmetz (1996) (Correct)
is based on the evaluation of silhouettes from **multiple** camera views [1]4]5] shape from silhouettes,
silhouette contour points as the angle-varying **distance** C(from the projection of the rotational axis
1 INTRODUCTION Recently the need for realistic 3D **models** of real objects is growing in a variety of
ftp.tnt.uni-hannover.de/pub/papers/1996/ICIP96-WN.ps.gz

Improving Equipment Maintainability via Alarm Threshold Optimization - Weiss (Correct)
possible to have a phase due to interrupts on **multiple** devices, even if none of the devices actually
systems used to manage and maintain the AT&T long **distance** network. In this paper we describe an effort
www.cs.rutgers.edu/~gweiss/papers/threshold.ps

Physical System Modelling Languages: From ALLAN to Modelica - Jeandel, Boudaud (1997) (Correct) (1 citation)

Physical Systemmodellinglanguages :From Allan To **Modelica** Alexandre Jeandel, Fabrice Boudaud Gaz De
- FRANCE alexandre.jeandel@edfgdf.fr ABSTRACT **Modelling** and simulation play a number of roles in
and rapidity. Studies are even more effective if **models** developed on previous occasions can be stored
www.dynasim.se/Modelica/papers/p303.ps

Fair SMG and Linear Time Model Checking - Barringer, Fisher, Gough (1989) (Correct) (3 citations)
Fair SMG and Linear Time **Model** Checking Howard Barringer, Michael D. Fisher and
is a system designed to generate a finite state **model** of a program from the program itself and an
for the programming language. This finite state **model** can then be **model-checked** to verify desired
www.doc.mmu.ac.uk/STAFF/michael/mdf-pubs/fair-smg.ps

A General NP-Completeness Theorem - Megiddo (1993) (Correct) (5 citations)
Nimrod Megiddo Revised) Abstract. A detailed **model** of a random access computation over an abstract
theorem and recent results in the real number **model** by Blum, Shub and Smale. 1. Introduction Blum,
Introduction Blum, Shub and Smale [2] formalized a **model** of computation over a general ring. They proved
www.math.tau.ac.il/~megiddo/.psfiles/smalecop.ps.gz

Characterization of Contention in Real Relational Databases - Singhal, Smith (1994) (Correct)
like the database size, data distribution on **multiple** nodes, data replication, and the access pattern.
attempts to minimize a measure of the total **distance** of the the data-curve over all points (a measure
Most of this literature uses either analytic **modeling** or random number driven simulation, and
ic.eecs.berkeley.edu/pub/Memos_ERL/csd94-801.ps.Z

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[Mixture Models and the EM Algorithm for Object Recognition within.. - Utans \(1993\) \(Correct\)](#)

or features that maximally discriminate between **multiple** stored **models**. Then, the training data derived ffl 1-510-642-4274 ffl FAX 1-510-643-7684 Mixture **Models** and the EM Algorithm for Object Recognition use the problem of part labelling in the context of **model** based object recognition where **models** are stored ftp.icsi.berkeley.edu/pub/techreports/1993/tr-93-004.ps.gz

[Analysis of Approximate Nearest Neighbor Searching with.. - Maneewongvatana, Mount \(1999\) \(Correct\) \(1 citation\)](#)

space is R^d real d -dimensional space, where **distances** are measured using any Minkowski **Lm distance** the expected query time on the training points. We **model** query processing as the problem of eliminating Given a collection of query points, we can **model** any stage of the nearest neighbor algorithm as a ftp.cs.umd.edu/pub/faculty/mount/Papers/dimacs99.ps.gz

[Networked Information Retrieval as Distributed Problem Solving - Oates, Prasad, Lesser \(1994\) \(Correct\) \(1 citation\)](#)

which a query is to run. Users may have access to **multiple** corpora managed by a set of information servers retrieval (IR) systems to re-evaluate their **model** of the world. Early IR systems assumed that users should be inferred. 1 For a more formal **model** that characterizes the environment and problem archive.cs.umbc.edu/pub/cikm/1994/ia/papers/oates.ps

[The Network Architecture of the Connection Machine CM-5 - Leiserson, Abuhamdeh.. \(1994\) \(Correct\) \(161 citations\)](#)

best aspects of SIMD (single instruction path, **multiple** data path) and MIMD (**multiple** instruction path, March 21, 1994 Abstract The Connection Machine **Model** CM-5 Supercomputer is a massively parallel a hypercube or a mesh. The Connection Machine **Model** CM-5 Supercomputer has three networks, however, www.ifi.uio.no/~oddvar/bib/papers/Lei+92.ps

[Interprocedural Array Data-Flow Analysis for Cache Coherence - Choi, Yew \(1995\) \(Correct\) \(3 citations\)](#)

or WAR (write-after-read) dependences among the **multiple** words in the same cache line can cause a false we first describe our parallel program execution **model** and introduce a stale reference condition which 4 concludes the paper. 1.1 Parallel execution **model** The execution of a parallel program can be polaris.cs.uiuc.edu/reports/1427.ps.gz

[Multi-Channel Image Identification and Restoration Using.. - Tom, Lay, Katsaggelos \(1995\) \(Correct\) \(5 citations\)](#)

use the term multi-channel images to define the **multiple** image planes (channels) which are typically true as well for A being complex. The key to this **proof** lies in the Cauchy-Riemann theorem [30] which Structure Mathematically, it is common practice to **model** the degradation process as $[9] y = Dx + v$ www.ece.nwu.edu/~briant/papers/mc-2.ps.gz

[Bayesian Phylogenetic Inference via Markov Chain Monte Carlo .. - Mau, Newton, Larget \(1998\) \(Correct\) \(5 citations\)](#)

the likelihood function from leaf data at **multiple** sites, we must marginalize (1) over all values popular methods, one can categorize those using **distance** matrices (e.g. Fitch and Margoliash, 1967 the corresponding set of organisms, a stochastic **model** for these data, and a prior distribution on the ftp.stat.wisc.edu/pub/newton/lastfinal.ps

[acQuire-macros: An Algorithm for Automatically Learning.. - McGovern \(1998\) \(Correct\) \(2 citations\)](#)

Learning hierarchical control structure for **multiple** tasks and changing environments. In From animals bit. The robot has simulated sonars to sense the **distance** to the nearest wall in each of five fixed learning with a hierarchy of abstract **models**. In Proceedings of the Tenth National Conference www.anw.cs.umass.edu/~amy/pubs/mcgovern_nips98_workshop.ps.gz

[Periodic Retrieval of Videos from Disk Arrays - Özden, Rastogi, Silberschatz \(1997\) \(Correct\) \(1 citation\)](#)

We present two schemes that stripe videos across **multiple** disks in order to distribute the workload

each retrieval unit is striped over all the disks. **Proofs** of theorems in the paper can be found in [12]2. a period. The period P_i for a video V_i is the **distance** (in rounds) between rounds in which successive
www.bell-labs.com/user/avi/publication-dir/periodic.ps

Face Recognition using Mixture-Distance and Raw Images - Steve Lawrence (Correct)

The results show that even in the absence of **multiple** training examples for each class, it is and Ingemar Cox, Face Recognition Using Mixture-Distance and Raw Images, 1997 IEEE International on local second-order statistics as estimated by **modeling** the training data with a mixture of normal
www.neci.nj.nec.com/homepages/lawrence/papers/mixture-smc97/mixture-smc97-a4.ps.gz

A Loop-Free Algorithm Based On Predecessor Information - Murthy, Garcia-Luna-Aceves (Correct)

the need for internodal synchronization spanning **multiple** hops or the specification of complete path interneighbor synchronization mechanism. A detailed **proof** of correctness is presented and its complexity is operates by specifying the second-to-last hop and **distance** to each destination in addition, it uses an
ftp.cse.ucsc.edu/pub/reinas/papers/shree.ic3n.ps.gz

ANALYSIS OF 7 NEARBY OPEN CLUSTERS USING HIPPARCOS DATA N. - Arenou Turon (Correct)

by a few degrees, or with a separation being a **multiple** of the basic angle between the two fields of the membership of stars. The mean cluster **distances** have been derived from intermediate Hipparcos from the calibration in luminosity of a solar **model** calculated with updated input physics (Lebreton
wwwhip.obspm.fr/~arenou/PS-papers/Venise/poster05_07.ps

Selected Topics in Computational Biology - DasGupta, Wang (1998) (Correct)

evolutionary trees: construction and comparison **multiple** sequence alignment problems. 1 Introduction The showed this problem to be NP-hard (an erroneous **proof** of the NP-hardness of the nni **distance** between construction and comparison, computing syntenic **distances** between multi-chromosome genomes, and **multiple**
crab.rutgers.edu/~bhaskar/resume/publ/papers/lwang.chap4.ps.Z

A Path-Finding Algorithm for Loop-Free Routing - Garcia-Luna-Aceves, Murthy (1997) (Correct) (5 citations)

the need for internodal synchronization spanning **multiple** hops or the specification of complete or to eliminate temporary routing loops. A detailed **proof** of LPA's correctness and loop-freedom property is operates by specifying the second-to-last hop and **distance** to each destination this feature is used to
www.cse.ucsc.edu/research/ccrg/publications/shree.ton.ps.gz

Achieving a Fitts Law Relationship for Visual Guided Reaching - Ferrier (1998) (Correct)

movements. Fine or accurate motions may require **multiple** corrective movements and hence the increase in relationship between the motion time, the target **distance**, and the target width. These studies suggest tracking methods require a sufficiently accurate **model** of the dynamics and accurate initialization
mechatron.me.wisc.edu/papers/iccv98.ps

Modelling Moving Objects in Multimedia Databases - Nabil, Ngu, Shepherd (1997) (Correct)

and the time-varying relationships between **multiple** moving objects in a scene. Several new query developed a higher-level representation using **distance** and direction, which we describe in Section 2. **Modelling Moving Objects in Multimedia Databases**
www.cse.unsw.edu.au/~jas/papers/dasfaa97b/paper.ps.gz

Evolving Ant Colony Optimization - Botee, Bonabeau (1998) (Correct) (3 citations)

to a classical stochastic greedy algorithm with **multiple** starting points because ants are initially ant k when ant k is currently on city i . 2) The **distance** d_{ij} between i and j . $d_{ij} = d_{ji}$ for a N. 1969) Distribution management: mathematical **modeling** and practical analysis. Op. Res. Quart. 20,
www.santafe.edu/sfi/publications/Working-Papers/99-01-009.ps.gz

Compton Observatory Osse Observations Of Supernova 1991t - Leising Johnson (Correct)

cm Gamma2 s Gamma1 depending on how the **multiple** observations are combined, for the 847 keV line supernova SN 1987A (Hamuy et al. 1988) Definitive **proof** awaited the detection of the signature fl-ray in the interval 66 to 79 days post-explosion. The **distance** to SN 1991T is quite uncertain, but these limits
osse-www.nrl.navy.mil/print60.ps

Quo Vadis - Adaptive Heuristics for Routing in Large.. - Mikler, Wong, Honavar (1995) (Correct)

as their spatio-temporal dynamics to optimize **multiple** performance criteria through cooperation among and reliable information transfer over very long **distances** has led to unprecedented expansion of such arrival of messages. Assuming network nodes to be **modeled** as M/M/1 queues [Jain, 1991 Robertazzi, 1990]
www.cs.iastate.edu/tech-reports/TR95-10.ps

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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. **Model-checking real-time concurrent systems**
Romanovsky, I.;
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annual Conference on
26-29 Nov. 2001 Page(s):439
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- ☐ 2. **Symbolic model checking for self-stabilizing algorithms**
Tsuchiya, T.; Nagano, S.; Paidi, R.B.; Kikuno, T.;
Parallel and Distributed Systems, IEEE Transactions on
Volume 12, Issue 1, Jan. 2001 Page(s):81 - 95
Digital Object Identifier 10.1109/71.899941
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(564 KB\)](#) IEEE JNL
- ☐ 3. **jMocha: a model checking tool that exploits design structure**
Alur, R.; de Alfaro, L.; Grosu, R.; Henzinger, T.A.; Kang, M.; Kirsch, C.M.; Majumdar, F.; Wang, B.Y.;
Software Engineering, 2001. ICSE 2001. Proceedings of the 23rd International 12-19 May 2001 Page(s):835 - 836
Digital Object Identifier 10.1109/ICSE.2001.919196
[AbstractPlus](#) | Full Text: [PDF\(196 KB\)](#) IEEE CNF
- ☐ 4. **A symbolic model checker for testing ASTRAL real-time specifications**
Zhe Dang; Kemmerer, R.A.;
Real-Time Computing Systems and Applications, 1999. RTCSA '99. Sixth International Conference on
13-15 Dec. 1999 Page(s):174 - 181
Digital Object Identifier 10.1109/RTCSA.1999.811215
[AbstractPlus](#) | Full Text: [PDF\(708 KB\)](#) IEEE CNF
- ☐ 5. **Model checking large software specifications**
Chan, W.; Anderson, R.J.; Beame, P.; Burns, S.; Modugno, F.; Notkin, D.; Rees, R.;
Software Engineering, IEEE Transactions on
Volume 24, Issue 7, July 1998 Page(s):498 - 520
Digital Object Identifier 10.1109/32.708566
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(488 KB\)](#) IEEE JNL
- ☐ 6. **Providing early feedback in the development cycle through automated model checking to software architectures**
Barber, K.S.; Graser, T.; Holt, J.;
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annual Conference on

Conference on
26-29 Nov. 2001 Page(s):341 - 345
[AbstractPlus](#) | Full Text: [PDF\(605 KB\)](#) IEEE CNF

- ☐ 7. **To model check or not to model check**
Saxena, N.; Baumgartner, J.; Saha, A.; Abraham, J.;
Computer Design: VLSI in Computers and Processors, 1998. ICCD '98. Proceedings. International Conference on
5-7 Oct. 1998 Page(s):314 - 320
Digital Object Identifier 10.1109/ICCD.1998.727067
[AbstractPlus](#) | Full Text: [PDF\(156 KB\)](#) IEEE CNF

- ☐ 8. **Partial-order methods for model checking: from linear time to branching**
Willem, B.; Wolper, P.;
Logic in Computer Science, 1996. LICS '96. Proceedings., Eleventh Annual IEEE Conference on
27-30 July 1996 Page(s):294 - 303
Digital Object Identifier 10.1109/LICS.1996.561357
[AbstractPlus](#) | Full Text: [PDF\(920 KB\)](#) IEEE CNF

- ☐ 9. **The symbolic model-checking for real-time systems**
Yamane, S.;
Real-Time Systems, 1996., Proceedings of the Eighth Euromicro Workshop on
12-14 June 1996 Page(s):108 - 113
Digital Object Identifier 10.1109/EMWRTS.1996.557829
[AbstractPlus](#) | Full Text: [PDF\(460 KB\)](#) IEEE CNF

- ☐ 10. **Feasibility of model checking software requirements: a case study**
Sreemani, T.; Atlee, J.M.;
Computer Assurance, 1996. COMPASS '96, 'Systems Integrity. Software Safe Security'. Proceedings of the Eleventh Annual Conference on
17-21 June 1996 Page(s):77 - 88
Digital Object Identifier 10.1109/CMPASS.1996.507877
[AbstractPlus](#) | Full Text: [PDF\(1040 KB\)](#) IEEE CNF

- ☐ 11. **An efficient algorithm for real-time symbolic model checking**
Frossl, J.; Gerlach, J.; Kropf, T.;
European Design and Test Conference, 1996. ED&TC 96. Proceedings
11-14 March 1996 Page(s):15 - 20
Digital Object Identifier 10.1109/EDTC.1996.494120
[AbstractPlus](#) | Full Text: [PDF\(620 KB\)](#) IEEE CNF

- ☐ 12. **Modeling and marshaling: making tests from model checker counterexamples**
Black, P.E.;
Digital Avionics Systems Conferences, 2000. Proceedings. DASC. The 19th Volume 1, 7-13 Oct. 2000 Page(s):1B3/1 - 1B3/6 vol.1
Digital Object Identifier 10.1109/DASC.2000.886880
[AbstractPlus](#) | Full Text: [PDF\(388 KB\)](#) IEEE CNF

- ☐ 13. **Coverage estimation for symbolic model checking**
Hoskote, Y.; Kam, T.; Pei-Hsin Ho; Xudong Zhao;
Design Automation Conference, 1999. Proceedings. 36th
21-25 June 1999 Page(s):300 - 305
Digital Object Identifier 10.1109/DAC.1999.781330
[AbstractPlus](#) | Full Text: [PDF\(584 KB\)](#) IEEE CNF

- ☐ 14. **Symbolic model checking: 10²⁰ states and beyond**
Burch, J.R.; Clarke, E.M.; McMillan, K.L.; Dill, D.L.; Hwang, L.J.;
Logic in Computer Science, 1990. LICS '90, Proceedings., Fifth Annual IEEE Conference on
4-7 June 1990 Page(s):428 - 439
Digital Object Identifier 10.1109/LICS.1990.113767
[AbstractPlus](#) | Full Text: [PDF\(812 KB\)](#) IEEE CNF

- ☐ **15. Model checking with multi-valued temporal logics**
Chechik, M.; Easterbrook, S.; Devereux, B.;
Multiple-Valued Logic, 2001. Proceedings. 31st IEEE International Symposium
22-24 May 2001 Page(s):187 - 192
Digital Object Identifier 10.1109/ISMVL.2001.924571
[AbstractPlus](#) | Full Text: [PDF\(480 KB\)](#) IEEE CNF
- ☐ **16. Automatic verification of fault tolerance using model checking**
Yokogawa, T.; Tsuchiya, T.; Kikuno, T.;
Dependable Computing, 2001. Proceedings. 2001 Pacific Rim International Sy
17-19 Dec. 2001 Page(s):95 - 102
Digital Object Identifier 10.1109/PRDC.2001.992685
[AbstractPlus](#) | Full Text: [PDF\(740 KB\)](#) IEEE CNF
- ☐ **17. Model checking of concurrent system with SDL⁺ specification**
Blaskovic, B.; Dembitz, S.; Knezevic, P.;
Electrotechnical Conference, 2000. MELECON 2000. 10th Mediterranean
Volume 1, 29-31 May 2000 Page(s):77 - 80 vol.1
Digital Object Identifier 10.1109/MELCON.2000.880372
[AbstractPlus](#) | Full Text: [PDF\(356 KB\)](#) IEEE CNF
- ☐ **18. Using model checking to generate tests from specifications**
Ammann, P.E.; Black, P.E.; Majurski, W.;
Formal Engineering Methods, 1998. Proceedings. Second International Confer
9-11 Dec. 1998 Page(s):46 - 54
Digital Object Identifier 10.1109/ICFEM.1998.730569
[AbstractPlus](#) | Full Text: [PDF\(208 KB\)](#) IEEE CNF
- ☐ **19. Mapping temporal planning constraints into timed automata**
Khatib, L.; Muscettola, N.; Havelund, K.;
Temporal Representation and Reasoning, 2001. TIME 2001. Proceedings. Eig
Symposium on
14-16 June 2001 Page(s):21 - 27
Digital Object Identifier 10.1109/TIME.2001.930693
[AbstractPlus](#) | Full Text: [PDF\(508 KB\)](#) IEEE CNF
- ☐ **20. Combining static analysis and model checking for software analysis**
Brat, G.; Visser, W.;
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annu
Conference on
26-29 Nov. 2001 Page(s):262 - 269
[AbstractPlus](#) | Full Text: [PDF\(833 KB\)](#) IEEE CNF
- ☐ **21. Exploiting heap symmetries in explicit-state model checking of software**
Iosif, R.;
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annu
Conference on
26-29 Nov. 2001 Page(s):254 - 261
[AbstractPlus](#) | Full Text: [PDF\(803 KB\)](#) IEEE CNF
- ☐ **22. Using model checking to analyze network vulnerabilities**
Ritchey, R.W.; Ammann, P.;
Security and Privacy, 2000. S&P 2000. Proceedings. 2000 IEEE Symposium c
14-17 May 2000 Page(s):156 - 165
Digital Object Identifier 10.1109/SECPRI.2000.848453
[AbstractPlus](#) | Full Text: [PDF\(132 KB\)](#) IEEE CNF
- ☐ **23. Action Language: a specification language for model checking reactive s**
Bultan, T.;
Software Engineering, 2000. Proceedings of the 2000 International Conferenc
4-11 June 2000 Page(s):335 - 344

Digital Object Identifier 10.1109/ICSE.2000.870424

[AbstractPlus](#) | Full Text: [PDF\(884 KB\)](#) IEEE CNF

- ☐ **24. v-Promela: a visual, object-oriented language for SPIN**
Leue, S.; Holzmann, G.;
Object-Oriented Real-Time Distributed Computing, 1999. (ISORC '99) Proceedings
International Symposium on
2-5 May 1999 Page(s):14 - 23
Digital Object Identifier 10.1109/ISORC.1999.776345
[AbstractPlus](#) | Full Text: [PDF\(100 KB\)](#) IEEE CNF

- ☐ **25. Abstracting formal specifications to generate software tests via model checking**
Ammann, P.; Black, P.E.;
Digital Avionics Systems Conference, 1999. Proceedings. 18th
Volume 2, 24-29 Oct. 1999 Page(s):10.A.6-1 - 10.A.6-10 vol.2
Digital Object Identifier 10.1109/DASC.1999.822091
[AbstractPlus](#) | Full Text: [PDF\(804 KB\)](#) IEEE CNF

- ☐ **26. Model checking: its basics and reality**
Fujita, M.;
Design Automation Conference 1998. Proceedings of the ASP-DAC '98. Asia
10-13 Feb. 1998 Page(s):217 - 222
Digital Object Identifier 10.1109/ASPDAC.1998.669449
[AbstractPlus](#) | Full Text: [PDF\(532 KB\)](#) IEEE CNF

- ☐ **27. Formal verification of real-time software by symbolic model-checker**
Nakamura, K.; Yamane, S.;
Application of Concurrency to System Design, 1998. Proceedings., 1998 International
Conference on
23-26 March 1998 Page(s):99 - 108
Digital Object Identifier 10.1109/CSD.1998.657543
[AbstractPlus](#) | Full Text: [PDF\(516 KB\)](#) IEEE CNF

- ☐ **28. Using MTBDDs for discrete timed symbolic model checking**
Kropf, T.; Ruf, J.;
European Design and Test Conference, 1997. ED&TC 97. Proceedings
17-20 March 1997 Page(s):182 - 187
Digital Object Identifier 10.1109/EDTC.1997.582356
[AbstractPlus](#) | Full Text: [PDF\(548 KB\)](#) IEEE CNF

- ☐ **29. Efficient model checking via the equational μ -calculus**
Bhat, G.; Cleaveland, R.;
Logic in Computer Science, 1996. LICS '96. Proceedings., Eleventh Annual International
Conference on
27-30 July 1996 Page(s):304 - 312
Digital Object Identifier 10.1109/LICS.1996.561358
[AbstractPlus](#) | Full Text: [PDF\(704 KB\)](#) IEEE CNF

- ☐ **30. Model checking of control-finite CSP programs**
Asai, K.; Matsuoka, S.; Yonezawa, A.;
System Sciences, 1993, Proceedings of the Twenty-Sixth Hawaii International Conference
on System Sciences, 5-8 Jan. 1993 Page(s):174 - 183 vol.2
Digital Object Identifier 10.1109/HICSS.1993.284112
[AbstractPlus](#) | Full Text: [PDF\(720 KB\)](#) IEEE CNF

- ☐ **31. A simple method for extracting models from protocol code**
Lie, D.; Chou, A.; Engler, D.; Dill, D.L.;
Computer Architecture, 2001. Proceedings. 28th Annual International Symposium on
30 June-4 July 2001 Page(s):192 - 203
Digital Object Identifier 10.1109/ISCA.2001.937448
[AbstractPlus](#) | Full Text: [PDF\(176 KB\)](#) IEEE CNF

- ☐ **32. Model checking Object-Z classes: Some experiments with FDR**
Kassel, G.; Smith, G.;
Software Engineering Conference, 2001. APSEC 2001. Eighth Asia-Pacific
4-7 Dec. 2001 Page(s):445 - 452
[AbstractPlus](#) | Full Text: [PDF\(740 KB\)](#) IEEE CNF

- ☐ **33. Formal verification of an industrial system-on-a-chip**
Hoon Choi; Myung-Kyoon Yim; Jae-Young Lee; Byeong-Whee Yun; Yun-Tae I
Computer Design, 2000. Proceedings. 2000 International Conference on
17-20 Sept. 2000 Page(s):453 - 458
Digital Object Identifier 10.1109/ICCD.2000.878322
[AbstractPlus](#) | Full Text: [PDF\(652 KB\)](#) IEEE CNF

- ☐ **34. Real-time symbolic model checking for hard real-time systems**
Tachi, Y.; Yamane, S.;
Real-Time Computing Systems and Applications, 1999. RTCSA '99. Sixth Inte
Conference on
13-15 Dec. 1999 Page(s):496 - 499
Digital Object Identifier 10.1109/RTCSA.1999.811305
[AbstractPlus](#) | Full Text: [PDF\(264 KB\)](#) IEEE CNF

- ☐ **35. Analysis and verification of multi-agent interaction protocols**
Wu Wen; Mizoguchi, F.;
Software Engineering Conference, 1999. (APSEC '99) Proceedings. Sixth Asia
7-10 Dec. 1999 Page(s):252 - 259
Digital Object Identifier 10.1109/APSEC.1999.809610
[AbstractPlus](#) | Full Text: [PDF\(580 KB\)](#) IEEE CNF

- ☐ **36. Model checking the secure electronic transaction (SET) protocol**
Lu, S.; Smolka, S.A.;
Modeling, Analysis and Simulation of Computer and Telecommunication Syste
Proceedings. 7th International Symposium on
24-28 Oct. 1999 Page(s):358 - 364
Digital Object Identifier 10.1109/MASCOT.1999.805074
[AbstractPlus](#) | Full Text: [PDF\(72 KB\)](#) IEEE CNF

- ☐ **37. Formal verification of a snoop-based cache coherence protocol using sy
checking**
Srinivasan, S.; Chhabra, P.S.; Jaini, P.K.; Aziz, A.; John, L.;
VLSI Design, 1999. Proceedings. Twelfth International Conference On
7-10 Jan. 1999 Page(s):288 - 293
Digital Object Identifier 10.1109/ICVD.1999.745162
[AbstractPlus](#) | Full Text: [PDF\(56 KB\)](#) IEEE CNF

- ☐ **38. An approach to verify a large scale system-on-a-chip using symbolic mo**
Takayama, K.; Satoh, T.; Nakata, T.; Hirose, F.;
Computer Design: VLSI in Computers and Processors, 1998. ICCD '98. Proce
International Conference on
5-7 Oct. 1998 Page(s):308 - 313
Digital Object Identifier 10.1109/ICCD.1998.727066
[AbstractPlus](#) | Full Text: [PDF\(96 KB\)](#) IEEE CNF

- ☐ **39. Approximate Reachability Don't Cares for CTL model checking**
In-Ho Moon; Jae-Young Jang; Hachtel, G.D.; Somenzi, F.; Jun Yuan; Pixley, C
Computer-Aided Design, 1998. ICCAD 98. Digest of Technical Papers. 1998 II
International Conference on
8-12 Nov 1998 Page(s):351 - 358
[AbstractPlus](#) | Full Text: [PDF\(892 KB\)](#) IEEE CNF

- ☐ **40. Validation with guided search of the state space**
Yang, C.H.; Dill, D.L.;

Design Automation Conference, 1998. Proceedings
15-19 Jun 1998 Page(s):599 - 604

[AbstractPlus](#) | Full Text: [PDF\(596 KB\)](#) IEEE CNF

- ☐ **41. Formal verification of VHDL: the model checker CV**
Deharbe, D.; Shankar, S.; Clarke, E.M.;
Integrated Circuit Design, 1998. Proceedings. XI Brazilian Symposium on
30 Sept.-3 Oct. 1998 Page(s):95 - 98
Digital Object Identifier 10.1109/SBCCI.1998.715418
[AbstractPlus](#) | Full Text: [PDF\(64 KB\)](#) IEEE CNF

- ☐ **42. Forward model checking techniques oriented to buggy designs**
Iwashita, H.; Nakata, T.;
Computer-Aided Design, 1997. Digest of Technical Papers., 1997 IEEE/ACM I
Conference on
9-13 Nov. 1997 Page(s):400 - 404
Digital Object Identifier 10.1109/ICCAD.1997.643567
[AbstractPlus](#) | Full Text: [PDF\(420 KB\)](#) IEEE CNF

- ☐ **43. Analyzing a PowerPCTM 620 microprocessor silicon failure using model**
Raimi, R.; Lear, J.;
Test Conference, 1997. Proceedings., International
1-6 Nov. 1997 Page(s):964 - 973
Digital Object Identifier 10.1109/TEST.1997.639712
[AbstractPlus](#) | Full Text: [PDF\(784 KB\)](#) IEEE CNF

- ☐ **44. Quantitative analysis and model checking**
Huth, M.; Kwiatkowska, M.;
Logic in Computer Science, 1997. LICS '97. Proceedings., 12th Annual IEEE S
29 June-2 July 1997 Page(s):111 - 122
Digital Object Identifier 10.1109/LICS.1997.614940
[AbstractPlus](#) | Full Text: [PDF\(872 KB\)](#) IEEE CNF

- ☐ **45. Word level model checking-avoiding the Pentium FDIV error**
Clarke, E.M.; Khaira, M.; Zhao, X.;
Design Automation Conference Proceedings 1996, 33rd
3-7 June 1996 Page(s):645 - 648
[AbstractPlus](#) | Full Text: [PDF\(368 KB\)](#) IEEE CNF

- ☐ **46. An approach to hierarchy model checking via evaluating CTL hierarchica**
Zuan Zhang;
Test Symposium, 1995., Proceedings of the Fourth Asian
23-24 Nov. 1995 Page(s):45 - 49
Digital Object Identifier 10.1109/ATS.1995.485315
[AbstractPlus](#) | Full Text: [PDF\(384 KB\)](#) IEEE CNF

- ☐ **47. Model-checking for real-time systems**
Alur, R.; Courcoubetis, C.; Dill, D.;
Logic in Computer Science, 1990. LICS '90, Proceedings., Fifth Annual IEEE S
4-7 June 1990 Page(s):414 - 425
Digital Object Identifier 10.1109/LICS.1990.113766
[AbstractPlus](#) | Full Text: [PDF\(956 KB\)](#) IEEE CNF

- ☐ **48. Automatic verification of finite-state concurrent systems**
Clarke, E.M., Jr.;
Logic in Computer Science, 1994. LICS '94. Proceedings., Symposium on
4-7 July 1994 Page(s):126
Digital Object Identifier 10.1109/LICS.1994.316079
[AbstractPlus](#) | Full Text: [PDF\(52 KB\)](#) IEEE CNF

- ☐ **49. State-based model checking of event-driven system requirements**

Atlee, J.M.; Gannon, J.;
Software Engineering, IEEE Transactions on
Volume 19, Issue 1, Jan. 1993 Page(s):24 - 40
Digital Object Identifier 10.1109/32.210305
[AbstractPlus](#) | Full Text: [PDF](#)(1340 KB) IEEE JNL

- ☐ **50. Communication space reduction for formal verification of secure authentication protocols**
Kyoil Kim; Abraham, J.A.;
Advanced Issues of E-Commerce and Web-Based Information Systems, WEC International Workshop on.
21-22 June 2001 Page(s):225 - 227
Digital Object Identifier 10.1109/WECWIS.2001.933928
[AbstractPlus](#) | Full Text: [PDF](#)(328 KB) IEEE CNF
- ☐ **51. Coverage based test-case generation using model checkers**
Rayadurgam, S.; Heimdahl, M.P.E.;
Engineering of Computer Based Systems, 2001. ECBS 2001. Proceedings. Ei; International Conference and Workshop on the
17-20 April 2001 Page(s):83 - 91
Digital Object Identifier 10.1109/ECBS.2001.922409
[AbstractPlus](#) | Full Text: [PDF](#)(892 KB) IEEE CNF
- ☐ **52. Model checking of S3C2400X industrial embedded SOC product**
Hoon Choi; Byeongwhee Yun; Yuntae Lee; Hyunglae Roh;
Design Automation Conference, 2001. Proceedings
2001 Page(s):611 - 616
[AbstractPlus](#) | Full Text: [PDF](#)(676 KB) IEEE CNF
- ☐ **53. Formal verification of Statecharts using finite-state model checkers**
Qianchuan Zhao; Krogh, B.H.;
American Control Conference, 2001. Proceedings of the 2001
Volume 1, 25-27 June 2001 Page(s):313 - 318 vol.1
Digital Object Identifier 10.1109/ACC.2001.945563
[AbstractPlus](#) | Full Text: [PDF](#)(456 KB) IEEE CNF
- ☐ **54. Model checking UML statecharts**
Wei Dong; Ji Wang; Xuan Qi; Zhi-Chang Qi;
Software Engineering Conference, 2001. APSEC 2001. Eighth Asia-Pacific
4-7 Dec. 2001 Page(s):363 - 370
[AbstractPlus](#) | Full Text: [PDF](#)(803 KB) IEEE CNF
- ☐ **55. Model checking for an executable subset of UML**
Fei Xie; Levin, V.; Browne, J.C.;
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annual Conference on
26-29 Nov. 2001 Page(s):333 - 336
[AbstractPlus](#) | Full Text: [PDF](#)(467 KB) IEEE CNF
- ☐ **56. The Production Cell: an exercise in the formal verification of a UML model**
Lilius, J.; Palter, I.P.;
System Sciences, 2000. Proceedings of the 33rd Annual Hawaii International Conference on
Jan 4-7 2000 Page(s):10 pp. vol.1
[AbstractPlus](#) | Full Text: [PDF](#)(124 KB) IEEE CNF
- ☐ **57. Using model checking for verification of partitioning properties in integrated avionics**
Cofer, D.; Engstrom, E.; Weininger, N.; Penix, J.; Visser, W.;
Digital Avionics Systems Conferences, 2000. Proceedings. DASC. The 19th
Volume 1, 7-13 Oct. 2000 Page(s):1D2/1 - 1D210 vol.1
Digital Object Identifier 10.1109/DASC.2000.886889
[AbstractPlus](#) | Full Text: [PDF](#)(732 KB) IEEE CNF

- ☐ **58. Tabled resolution + constraints: a recipe for model checking real-time sy**
Xiaoqun Du; Ramakrishnan, C.R.; Smolka, S.A.;
Real-Time Systems Symposium, 2000. Proceedings. The 21st IEEE
27-30 Nov. 2000 Page(s):175 - 184
Digital Object Identifier 10.1109/REAL.2000.896007
[AbstractPlus](#) | Full Text: [PDF\(820 KB\)](#) IEEE CNF
- ☐ **59. Model checking programs**
Visser, W.; Havelund, K.; Brat, G.; Seungjoon Park;
Automated Software Engineering, 2000. Proceedings ASE 2000. The Fifteenth
International Conference on
11-15 Sept. 2000 Page(s):3 - 11
Digital Object Identifier 10.1109/ASE.2000.873645
[AbstractPlus](#) | Full Text: [PDF\(836 KB\)](#) IEEE CNF
- ☐ **60. Java model checking**
Park, D.Y.W.; Stern, U.; Skakkebaek, J.U.; Dill, D.L.;
Automated Software Engineering, 2000. Proceedings ASE 2000. The Fifteenth
International Conference on
11-15 Sept. 2000 Page(s):253 - 256
Digital Object Identifier 10.1109/ASE.2000.873671
[AbstractPlus](#) | Full Text: [PDF\(320 KB\)](#) IEEE CNF
- ☐ **61. Simulation strategy after model checking: experience in industrial SOC d**
Hoon Choi; Byeong-Whee Yun; Yun-Tae Lee;
High-Level Design Validation and Test Workshop, 2000. Proceedings. IEEE In
8-10 Nov. 2000 Page(s):77 - 79
Digital Object Identifier 10.1109/HLDVT.2000.889563
[AbstractPlus](#) | Full Text: [PDF\(264 KB\)](#) IEEE CNF
- ☐ **62. Automated translation of UML models of architectures for verification an
using SPIN**
Bose, P.;
Automated Software Engineering, 1999. 14th IEEE International Conference o
12-15 Oct. 1999 Page(s):102 - 109
Digital Object Identifier 10.1109/ASE.1999.802135
[AbstractPlus](#) | Full Text: [PDF\(76 KB\)](#) IEEE CNF
- ☐ **63. Hazard checking in pipelined processor designs using symbolic model c**
Schonherr, J.; Schreiber, I.; Fordran, E.; Straube, B.;
EUROMICRO Conference, 1999. Proceedings. 25th
Volume 1, 8-10 Sept. 1999 Page(s):75 - 78 vol.1
Digital Object Identifier 10.1109/EURMIC.1999.794450
[AbstractPlus](#) | Full Text: [PDF\(60 KB\)](#) IEEE CNF
- ☐ **64. Increasing efficiency of symbolic model checking by accelerating dynam
reordering**
Meinel, C.; Stangier, C.;
Design, Automation and Test in Europe Conference and Exhibition 1999. Proc
9-12 March 1999 Page(s):760 - 761
Digital Object Identifier 10.1109/DATE.1999.761218
[AbstractPlus](#) | Full Text: [PDF\(24 KB\)](#) IEEE CNF
- ☐ **65. Decoupling synchronization from local control for efficient symbolic moc
statecharts**
Chan, W.; Anderson, R.J.; Beame, P.; Jones, D.H.; Notkin, D.; Warner, W.E.;
Software Engineering, 1999. Proceedings of the 1999 International Conferenc
16-22 May 1999 Page(s):142 - 151
Digital Object Identifier 10.1109/ICSE.1999.841003
[AbstractPlus](#) | Full Text: [PDF\(972 KB\)](#) IEEE CNF

- ☐ **66. Using the ASTRAL model checker to analyze Mobile IP**
Zhe Dang; Kemmerer, R.A.;
Software Engineering, 1999. Proceedings of the 1999 International Conference
16-22 May 1999 Page(s):132 - 141
Digital Object Identifier 10.1109/ICSE.1999.841002
[AbstractPlus](#) | Full Text: [PDF\(860 KB\)](#) IEEE CNF

- ☐ **67. Applying model checking to concurrent object-oriented software**
Seung Mo Cho; Doo Hwan Bae; Sung Deok Cha; Young Gon Kim; Byung Kyu Kim; Kr, Y.K.A.; Kim, S.T.;
Autonomous Decentralized Systems, 1999. Integration of Heterogeneous Syst
Proceedings. The Fourth International Symposium on
21-23 March 1999 Page(s):380 - 383
Digital Object Identifier 10.1109/ISADS.1999.838464
[AbstractPlus](#) | Full Text: [PDF\(156 KB\)](#) IEEE CNF

- ☐ **68. A partial approach to model checking**
Godefroid, P.; Wolper, P.;
Logic in Computer Science, 1991. LICS '91., Proceedings of Sixth Annual IEEE
15-18 July 1991 Page(s):406 - 415
Digital Object Identifier 10.1109/LICS.1991.151664
[AbstractPlus](#) | Full Text: [PDF\(748 KB\)](#) IEEE CNF

- ☐ **69. Fast parallel algorithms for model checking using BDDs**
Lee, I.; Rajasekaran, S.;
Parallel Processing Symposium, 1993., Proceedings of Seventh International
13-16 April 1993 Page(s):444 - 448
Digital Object Identifier 10.1109/IPPS.1993.262834
[AbstractPlus](#) | Full Text: [PDF\(380 KB\)](#) IEEE CNF

- ☐ **70. Compositional model checking of Ada tasking programs**
Fischer, J.; Gerber, R.;
Computer Assurance, 1994. COMPASS '94 'Safety, Reliability, Fault Tolerance
and Real Time, Security'. Proceedings of the Ninth Annual Conference on
27 June-1 July 1994 Page(s):135 - 147
Digital Object Identifier 10.1109/COMPASS.1994.318460
[AbstractPlus](#) | Full Text: [PDF\(800 KB\)](#) IEEE CNF

- ☐ **71. Symbolic model checking for sequential circuit verification**
Burch, J.R.; Clarke, E.M.; Long, D.E.; McMillan, K.L.; Dill, D.L.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction
Volume 13, Issue 4, April 1994 Page(s):401 - 424
Digital Object Identifier 10.1109/43.275352
[AbstractPlus](#) | Full Text: [PDF\(2324 KB\)](#) IEEE JNL

- ☐ **72. Application of parametric model checking - the Root Contention protocol**
Bandini, G.; Spelberg, R.L.; de Rooij, R.C.H.; Toetenel, W.J.;
System Sciences, 2001. Proceedings of the 34th Annual Hawaii International (C
Jan 3-6 2001 Page(s):10 pp.
[AbstractPlus](#) | Full Text: [PDF\(220 KB\)](#) IEEE CNF

- ☐ **73. Formal verification of embedded real-time software in component-based frameworks**
Pao-Ann Hsiung; Win-Bin See; Trong-Yen Lee; Jih-Ming Fu; Sao-Jie Chen;
Software Engineering Conference, 2001. APSEC 2001. Eighth Asia-Pacific
4-7 Dec. 2001 Page(s):71 - 78
[AbstractPlus](#) | Full Text: [PDF\(861 KB\)](#) IEEE CNF

- ☐ **74. Three approximation techniques for ASTRAL symbolic model checking of real-time systems**
Zhe Dang; Kemmerer, R.A.;
Software Engineering, 2000. Proceedings of the 2000 International Conference

4-11 June 2000 Page(s):345 - 354

Digital Object Identifier 10.1109/ICSE.2000.870425

[AbstractPlus](#) | Full Text: [PDF](#)(1036 KB) IEEE CNF

- ☐ **75. Freedom, weakness, and determinism: from linear-time to branching-time**
Kupferman, O.; Vardit, M.Y.;
Logic in Computer Science, 1998. Proceedings. Thirteenth Annual IEEE Symp.
21-24 June 1998 Page(s):81 - 92
Digital Object Identifier 10.1109/LICS.1998.705645
[AbstractPlus](#) | Full Text: [PDF](#)(252 KB) IEEE CNF
- ☐ **76. An overview and application of model reduction techniques in formal ver**
Baumgartner, J.; Heyman, T.;
Performance, Computing and Communications, 1998. IPCCC '98., IEEE Interr
16-18 Feb. 1998 Page(s):165 - 171
Digital Object Identifier 10.1109/PCCC.1998.659939
[AbstractPlus](#) | Full Text: [PDF](#)(616 KB) IEEE CNF
- ☐ **77. Incremental CTL model checking using BDD subsetting**
Pardo, A.; Hachtel, G.D.;
Design Automation Conference, 1998. Proceedings
15-19 Jun 1998 Page(s):457 - 462
[AbstractPlus](#) | Full Text: [PDF](#)(640 KB) IEEE CNF
- ☐ **78. Partial model checking**
Andersen, H.R.;
Logic in Computer Science, 1995. LICS '95. Proceedings., Tenth Annual IEEE
26-29 June 1995 Page(s):398 - 407
Digital Object Identifier 10.1109/LICS.1995.523274
[AbstractPlus](#) | Full Text: [PDF](#)(668 KB) IEEE CNF
- ☐ **79. Automated modular specification and verification of real-time reactive sy**
Ostroff, J.S.;
Industrial-Strength Formal Specification Techniques, 1995. Proceedings., Wor
5-8 April 1995 Page(s):108 - 121
Digital Object Identifier 10.1109/WIFT.1995.515483
[AbstractPlus](#) | Full Text: [PDF](#)(1944 KB) IEEE CNF
- ☐ **80. Compositional and symbolic model-checking of real-time systems**
Larsen, K.G.; Pettersson, P.; Wang Yi;
Real-Time Systems Symposium, 1995. Proceedings., 16th IEEE
5-7 Dec. 1995 Page(s):76 - 87
Digital Object Identifier 10.1109/REAL.1995.495198
[AbstractPlus](#) | Full Text: [PDF](#)(988 KB) IEEE CNF
- ☐ **81. Bug identification of a real chip design by symbolic model checking**
Chen, B.; Yamazaki, M.; Fujita, M.;
European Design and Test Conference, 1994. EDAC, The European Conferer
Automation. ETC European Test Conference. EUROASIC, The European Eve
Design, Proceedings.
28 Feb.-3 March 1994 Page(s):132 - 136
Digital Object Identifier 10.1109/EDTC.1994.326886
[AbstractPlus](#) | Full Text: [PDF](#)(356 KB) IEEE CNF
- ☐ **82. Modeling programmable logic controllers for logic verification**
Moon, I.;
Control Systems Magazine, IEEE
Volume 14, Issue 2, April 1994 Page(s):53 - 59
Digital Object Identifier 10.1109/37.272781
[AbstractPlus](#) | Full Text: [PDF](#)(520 KB) IEEE JNL

- ☐ **83. Model checking in practice: the T9000 virtual channel processor**
Barrett, G.;
Software Engineering, IEEE Transactions on
Volume 21, Issue 2, Feb. 1995 Page(s):69 - 78
Digital Object Identifier 10.1109/32.345823
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(816 KB\)](#) IEEE JNL

- ☐ **84. E-process design and assurance using model checking**
Wang, W.; Hidvegi, Z.; Bailey, A.D., Jr.; Whinston, A.B.;
Computer
Volume 33, Issue 10, Oct. 2000 Page(s):48 - 53
Digital Object Identifier 10.1109/2.876292
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(212 KB\)](#) IEEE JNL

- ☐ **85. Automatic support for verification of secure transactions in distributed e using symbolic model checking**
Di Sciascio, E.; Donini, F.M.; Mongiello, M.; Piscitelli, G.;
Information Technology Interfaces, 2001. ITI 2001. Proceedings of the 23rd Int
Conference on
19-22 June 2001 Page(s):447 - 454 vol.1
Digital Object Identifier 10.1109/ITI.2001.938054
[AbstractPlus](#) | Full Text: [PDF\(616 KB\)](#) IEEE CNF

- ☐ **86. Using a model checker to test safety properties**
Ammann, P.; Wei Ding; Daling Xu;
Engineering of Complex Computer Systems, 2001. Proceedings. Seventh IEE
Conference on
11-13 June 2001 Page(s):212 - 221
Digital Object Identifier 10.1109/ICECCS.2001.930180
[AbstractPlus](#) | Full Text: [PDF\(728 KB\)](#) IEEE CNF

- ☐ **87. Events and constraints: a graphical editor for capturing logic requiremen**
Smith, M.H.; Holzmann, G.J.; Etessami, K.;
Requirements Engineering, 2001. Proceedings. Fifth IEEE International Sympo
27-31 Aug. 2001 Page(s):14 - 22
Digital Object Identifier 10.1109/ISRE.2001.948539
[AbstractPlus](#) | Full Text: [PDF\(712 KB\)](#) IEEE CNF

- ☐ **88. Introduction to generalized symbolic trajectory evaluation**
Jin Yang; Seger, C.-J.H.;
Computer Design, 2001. ICCD 2001. Proceedings. 2001 International Confere
23-26 Sept. 2001 Page(s):360 - 365
Digital Object Identifier 10.1109/ICCD.2001.955052
[AbstractPlus](#) | Full Text: [PDF\(496 KB\)](#) IEEE CNF

- ☐ **89. Automatic synthesis of the DC specifications of lip synchronisation prot**
Huadong Ma; Liang Li; Jianzhong Wang; Naijun Zhan;
Software Engineering Conference, 2001. APSEC 2001. Eighth Asia-Pacific
4-7 Dec. 2001 Page(s):371 - 378
[AbstractPlus](#) | Full Text: [PDF\(676 KB\)](#) IEEE CNF

- ☐ **90. An automated tool for analyzing Petri nets using Spin**
Gannod, G.C.; Gupta, S.;
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annu
Conference on
26-29 Nov. 2001 Page(s):404 - 407
[AbstractPlus](#) | Full Text: [PDF\(468 KB\)](#) IEEE CNF

- ☐ **91. Test-sequence generation from formal requirement models**
Rayadurgam, S.; Heimdahl, M.P.E.;
High Assurance Systems Engineering, 2001. Sixth IEEE International Sympos
22-24 Oct. 2001 Page(s):23 - 31

Digital Object Identifier 10.1109/HASE.2001.966804

[AbstractPlus](#) | Full Text: [PDF](#)(192 KB) IEEE CNF

- ☐ **92. Using a hardware model checker to verify software**
Edwards, S.A.; Ma, T.; Damiano, R.;
ASIC, 2001. Proceedings. 4th International Conference on
23-25 Oct. 2001 Page(s):85 - 90
Digital Object Identifier 10.1109/ICASIC.2001.982504
[AbstractPlus](#) | Full Text: [PDF](#)(599 KB) IEEE CNF

- ☐ **93. Verifying sequential behavior with model checking**
Biere, A.;
ASIC, 2001. Proceedings. 4th International Conference on
23-25 Oct. 2001 Page(s):29 - 32
Digital Object Identifier 10.1109/ICASIC.2001.982490
[AbstractPlus](#) | Full Text: [PDF](#)(438 KB) IEEE CNF

- ☐ **94. A model checking approach to evaluating system level dynamic power m
policies for embedded systems**
Shukla, S.K.; Gupta, R.K.;
High-Level Design Validation and Test Workshop, 2001. Proceedings. Sixth IE
7-9 Nov. 2001 Page(s):53 - 57
Digital Object Identifier 10.1109/HLDVT.2001.972807
[AbstractPlus](#) | Full Text: [PDF](#)(75 KB) IEEE CNF

- ☐ **95. Practical use of sequential ATPG for model checking: going the extra mil**
Hsiao, M.; Jain, J.;
High-Level Design Validation and Test Workshop, 2001. Proceedings. Sixth IE
7-9 Nov. 2001 Page(s):39 - 44
Digital Object Identifier 10.1109/HLDVT.2001.972805
[AbstractPlus](#) | Full Text: [PDF](#)(152 KB) IEEE CNF

- ☐ **96. Iterative abstraction-based CTL model checking**
Jae-Young Jang; In-Ho Moon; Hachtel, G.D.;
Design, Automation and Test in Europe Conference and Exhibition 2000. Proc
27-30 March 2000 Page(s):502 - 507
Digital Object Identifier 10.1109/DATE.2000.840831
[AbstractPlus](#) | Full Text: [PDF](#)(208 KB) IEEE CNF

- ☐ **97. Analysis of real-time code by model checking**
Guaspari, D.; Naydich, D.;
Digital Avionics Systems Conferences, 2000. Proceedings. DASC. The 19th
Volume 1, 7-13 Oct. 2000 Page(s):1D5/1 - 1D5/8 vol.1
Digital Object Identifier 10.1109/DASC.2000.886892
[AbstractPlus](#) | Full Text: [PDF](#)(572 KB) IEEE CNF

- ☐ **98. Implicit model checking: formal verification technique for large-scale dis**
Taeshin Park;
Computer-Aided Control System Design, 2000. CACSD 2000. IEEE Internati
on
25-27 Sept. 2000 Page(s):135 - 140
Digital Object Identifier 10.1109/CACSD.2000.900200
[AbstractPlus](#) | Full Text: [PDF](#)(456 KB) IEEE CNF

- ☐ **99. Validating executable controller specifications through formal model che**
Scillieri, J.J.; Butts, K.R.; Freudenberg, J.S.;
Computer-Aided Control System Design, 2000. CACSD 2000. IEEE Internati
on
25-27 Sept. 2000 Page(s):179 - 184
Digital Object Identifier 10.1109/CACSD.2000.900208
[AbstractPlus](#) | Full Text: [PDF](#)(456 KB) IEEE CNF


☐ 100. Verification of logic control design using SIPN and model checking: met study

Weng, X.; Litz, L.;

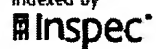
American Control Conference, 2000. Proceedings of the 2000

Volume 6, 28-30 June 2000 Page(s):4072 - 4076 vol.6

Digital Object Identifier 10.1109/ACC.2000.876987

[AbstractPlus](#) | Full Text: [PDF\(804 KB\)](#) IEEE CNF View: [1-100](#) | [101-200](#) | [201-300](#) | :

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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. **Model-checking real-time concurrent systems**
 Romanovsky, I.;
 Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annual Conference on
 26-29 Nov. 2001 Page(s):439
[AbstractPlus](#) | Full Text: [PDF\(175 KB\)](#) IEEE CNF
- ☐ 2. **Symbolic model checking for self-stabilizing algorithms**
 Tsuchiya, T.; Nagano, S.; Paidi, R.B.; Kikuno, T.;
 Parallel and Distributed Systems, IEEE Transactions on
 Volume 12, Issue 1, Jan. 2001 Page(s):81 - 95
 Digital Object Identifier 10.1109/71.899941
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(564 KB\)](#) IEEE JNL
- ☐ 3. **jMocha: a model checking tool that exploits design structure**
 Alur, R.; de Alfaro, L.; Grosu, R.; Henzinger, T.A.; Kang, M.; Kirsch, C.M.; Majumdar, F.; Wang, B.Y.;
 Software Engineering, 2001. ICSE 2001. Proceedings of the 23rd International 12-19 May 2001 Page(s):835 - 836
 Digital Object Identifier 10.1109/ICSE.2001.919196
[AbstractPlus](#) | Full Text: [PDF\(196 KB\)](#) IEEE CNF
- ☐ 4. **A symbolic model checker for testing ASTRAL real-time specifications**
 Zhe Dang; Kemmerer, R.A.;
 Real-Time Computing Systems and Applications, 1999. RTCSA '99. Sixth International Conference on
 13-15 Dec. 1999 Page(s):174 - 181
 Digital Object Identifier 10.1109/RTCSA.1999.811215
[AbstractPlus](#) | Full Text: [PDF\(708 KB\)](#) IEEE CNF
- ☐ 5. **Model checking large software specifications**
 Chan, W.; Anderson, R.J.; Beame, P.; Burns, S.; Modugno, F.; Notkin, D.; Rees, R.;
 Software Engineering, IEEE Transactions on
 Volume 24, Issue 7, July 1998 Page(s):498 - 520
 Digital Object Identifier 10.1109/32.708566
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(488 KB\)](#) IEEE JNL
- ☐ 6. **Providing early feedback in the development cycle through automated model checking to software architectures**
 Barber, K.S.; Graser, T.; Holt, J.;
 Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annual

Conference on
26-29 Nov. 2001 Page(s):341 - 345
[AbstractPlus](#) | Full Text: [PDF](#)(605 KB) IEEE CNF

- ☐ 7. **To model check or not to model check**
Saxena, N.; Baumgartner, J.; Saha, A.; Abraham, J.;
Computer Design: VLSI in Computers and Processors, 1998. ICCD '98. Proceedings of the International Conference on
5-7 Oct. 1998 Page(s):314 - 320
Digital Object Identifier 10.1109/ICCD.1998.727067
[AbstractPlus](#) | Full Text: [PDF](#)(156 KB) IEEE CNF

- ☐ 8. **Partial-order methods for model checking: from linear time to branching**
Willems, B.; Wolper, P.;
Logic in Computer Science, 1996. LICS '96. Proceedings., Eleventh Annual IEEE Symposium on
27-30 July 1996 Page(s):294 - 303
Digital Object Identifier 10.1109/LICS.1996.561357
[AbstractPlus](#) | Full Text: [PDF](#)(920 KB) IEEE CNF

- ☐ 9. **The symbolic model-checking for real-time systems**
Yamane, S.;
Real-Time Systems, 1996., Proceedings of the Eighth Euromicro Workshop on
12-14 June 1996 Page(s):108 - 113
Digital Object Identifier 10.1109/EMWRTS.1996.557829
[AbstractPlus](#) | Full Text: [PDF](#)(460 KB) IEEE CNF

- ☐ 10. **Feasibility of model checking software requirements: a case study**
Sreemani, T.; Atlee, J.M.;
Computer Assurance, 1996. COMPASS '96, 'Systems Integrity. Software Safe Security'. Proceedings of the Eleventh Annual Conference on
17-21 June 1996 Page(s):77 - 88
Digital Object Identifier 10.1109/COMPASS.1996.507877
[AbstractPlus](#) | Full Text: [PDF](#)(1040 KB) IEEE CNF

- ☐ 11. **An efficient algorithm for real-time symbolic model checking**
Frossl, J.; Gerlach, J.; Kropf, T.;
European Design and Test Conference, 1996. ED&TC 96. Proceedings
11-14 March 1996 Page(s):15 - 20
Digital Object Identifier 10.1109/EDTC.1996.494120
[AbstractPlus](#) | Full Text: [PDF](#)(620 KB) IEEE CNF

- ☐ 12. **Modeling and marshaling: making tests from model checker counterexamples**
Black, P.E.;
Digital Avionics Systems Conferences, 2000. Proceedings. DASC. The 19th Volume 1, 7-13 Oct. 2000 Page(s):1B3/1 - 1B3/6 vol.1
Digital Object Identifier 10.1109/DASC.2000.886880
[AbstractPlus](#) | Full Text: [PDF](#)(388 KB) IEEE CNF

- ☐ 13. **Coverage estimation for symbolic model checking**
Hoskote, Y.; Kam, T.; Pei-Hsin Ho; Xudong Zhao;
Design Automation Conference, 1999. Proceedings. 36th
21-25 June 1999 Page(s):300 - 305
Digital Object Identifier 10.1109/DAC.1999.781330
[AbstractPlus](#) | Full Text: [PDF](#)(584 KB) IEEE CNF

- ☐ 14. **Symbolic model checking: 10^{20} states and beyond**
Burch, J.R.; Clarke, E.M.; McMillan, K.L.; Dill, D.L.; Hwang, L.J.;
Logic in Computer Science, 1990. LICS '90, Proceedings., Fifth Annual IEEE Symposium on
4-7 June 1990 Page(s):428 - 439
Digital Object Identifier 10.1109/LICS.1990.113767
[AbstractPlus](#) | Full Text: [PDF](#)(812 KB) IEEE CNF

- ☐ **15. Model checking with multi-valued temporal logics**
Chechik, M.; Easterbrook, S.; Devereux, B.;
Multiple-Valued Logic, 2001. Proceedings. 31st IEEE International Symposium
22-24 May 2001 Page(s):187 - 192
Digital Object Identifier 10.1109/ISMVL.2001.924571
[AbstractPlus](#) | Full Text: [PDF\(480 KB\)](#) IEEE CNF

- ☐ **16. Automatic verification of fault tolerance using model checking**
Yokogawa, T.; Tsuchiya, T.; Kikuno, T.;
Dependable Computing, 2001. Proceedings. 2001 Pacific Rim International Sy
17-19 Dec. 2001 Page(s):95 - 102
Digital Object Identifier 10.1109/PRDC.2001.992685
[AbstractPlus](#) | Full Text: [PDF\(740 KB\)](#) IEEE CNF

- ☐ **17. Model checking of concurrent system with SDL⁺ specification**
Blaskovic, B.; Dembitz, S.; Knezevic, P.;
Electrotechnical Conference, 2000. MELECON 2000. 10th Mediterranean
Volume 1, 29-31 May 2000 Page(s):77 - 80 vol.1
Digital Object Identifier 10.1109/MELCON.2000.880372
[AbstractPlus](#) | Full Text: [PDF\(356 KB\)](#) IEEE CNF

- ☐ **18. Using model checking to generate tests from specifications**
Ammann, P.E.; Black, P.E.; Majurski, W.;
Formal Engineering Methods, 1998. Proceedings. Second International Confer
9-11 Dec. 1998 Page(s):46 - 54
Digital Object Identifier 10.1109/ICFEM.1998.730569
[AbstractPlus](#) | Full Text: [PDF\(208 KB\)](#) IEEE CNF

- ☐ **19. Mapping temporal planning constraints into timed automata**
Khatib, L.; Muscettola, N.; Havelund, K.;
Temporal Representation and Reasoning, 2001. TIME 2001. Proceedings. Eig
Symposium on
14-16 June 2001 Page(s):21 - 27
Digital Object Identifier 10.1109/TIME.2001.930693
[AbstractPlus](#) | Full Text: [PDF\(508 KB\)](#) IEEE CNF

- ☐ **20. Combining static analysis and model checking for software analysis**
Brat, G.; Visser, W.;
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annu
Conference on
26-29 Nov. 2001 Page(s):262 - 269
[AbstractPlus](#) | Full Text: [PDF\(833 KB\)](#) IEEE CNF

- ☐ **21. Exploiting heap symmetries in explicit-state model checking of software**
Iosif, R.;
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annu
Conference on
26-29 Nov. 2001 Page(s):254 - 261
[AbstractPlus](#) | Full Text: [PDF\(803 KB\)](#) IEEE CNF

- ☐ **22. Using model checking to analyze network vulnerabilities**
Ritchey, R.W.; Ammann, P.;
Security and Privacy, 2000. S&P 2000. Proceedings. 2000 IEEE Symposium c
14-17 May 2000 Page(s):156 - 165
Digital Object Identifier 10.1109/SECPRI.2000.848453
[AbstractPlus](#) | Full Text: [PDF\(132 KB\)](#) IEEE CNF

- ☐ **23. Action Language: a specification language for model checking reactive s**
Bultan, T.;
Software Engineering, 2000. Proceedings of the 2000 International Conferenc
4-11 June 2000 Page(s):335 - 344

Digital Object Identifier 10.1109/ICSE.2000.870424

[AbstractPlus](#) | Full Text: [PDF\(884 KB\)](#) IEEE CNF

- ☐ **24. v-Promela: a visual, object-oriented language for SPIN**
Leue, S.; Holzmann, G.;
Object-Oriented Real-Time Distributed Computing, 1999. (ISORC '99) Proceedings
International Symposium on
2-5 May 1999 Page(s):14 - 23
Digital Object Identifier 10.1109/ISORC.1999.776345
[AbstractPlus](#) | Full Text: [PDF\(100 KB\)](#) IEEE CNF
- ☐ **25. Abstracting formal specifications to generate software tests via model checking**
Ammann, P.; Black, P.E.;
Digital Avionics Systems Conference, 1999. Proceedings. 18th
Volume 2, 24-29 Oct. 1999 Page(s):10.A.6-1 - 10.A.6-10 vol.2
Digital Object Identifier 10.1109/DASC.1999.822091
[AbstractPlus](#) | Full Text: [PDF\(804 KB\)](#) IEEE CNF
- ☐ **26. Model checking: its basics and reality**
Fujita, M.;
Design Automation Conference 1998. Proceedings of the ASP-DAC '98. Asia
10-13 Feb. 1998 Page(s):217 - 222
Digital Object Identifier 10.1109/ASPDAC.1998.669449
[AbstractPlus](#) | Full Text: [PDF\(532 KB\)](#) IEEE CNF
- ☐ **27. Formal verification of real-time software by symbolic model-checker**
Nakamura, K.; Yamane, S.;
Application of Concurrency to System Design, 1998. Proceedings., 1998 International
Conference on
23-26 March 1998 Page(s):99 - 108
Digital Object Identifier 10.1109/CSD.1998.657543
[AbstractPlus](#) | Full Text: [PDF\(516 KB\)](#) IEEE CNF
- ☐ **28. Using MTBDDs for discrete timed symbolic model checking**
Kropf, T.; Ruf, J.;
European Design and Test Conference, 1997. ED&TC 97. Proceedings
17-20 March 1997 Page(s):182 - 187
Digital Object Identifier 10.1109/EDTC.1997.582356
[AbstractPlus](#) | Full Text: [PDF\(548 KB\)](#) IEEE CNF
- ☐ **29. Efficient model checking via the equational μ -calculus**
Bhat, G.; Cleaveland, R.;
Logic in Computer Science, 1996. LICS '96. Proceedings., Eleventh Annual IEEE
Conference on
27-30 July 1996 Page(s):304 - 312
Digital Object Identifier 10.1109/LICS.1996.561358
[AbstractPlus](#) | Full Text: [PDF\(704 KB\)](#) IEEE CNF
- ☐ **30. Model checking of control-finite CSP programs**
Asai, K.; Matsuoka, S.; Yonezawa, A.;
System Sciences, 1993. Proceedings of the Twenty-Sixth Hawaii International
Conference on
Volume ii, 5-8 Jan. 1993 Page(s):174 - 183 vol.2
Digital Object Identifier 10.1109/HICSS.1993.284112
[AbstractPlus](#) | Full Text: [PDF\(720 KB\)](#) IEEE CNF
- ☐ **31. A simple method for extracting models from protocol code**
Lie, D.; Chou, A.; Engler, D.; Dill, D.L.;
Computer Architecture, 2001. Proceedings. 28th Annual International Symposium
30 June-4 July 2001 Page(s):192 - 203
Digital Object Identifier 10.1109/ISCA.2001.937448
[AbstractPlus](#) | Full Text: [PDF\(176 KB\)](#) IEEE CNF

- ☐ 32. **Model checking Object-Z classes: Some experiments with FDR**
Kassel, G.; Smith, G.;
Software Engineering Conference, 2001. APSEC 2001. Eighth Asia-Pacific
4-7 Dec. 2001 Page(s):445 - 452
[AbstractPlus](#) | Full Text: [PDF\(740 KB\)](#) IEEE CNF
- ☐ 33. **Formal verification of an industrial system-on-a-chip**
Hoon Choi; Myung-Kyoon Yim; Jae-Young Lee; Byeong-Whee Yun; Yun-Tae I
Computer Design, 2000. Proceedings. 2000 International Conference on
17-20 Sept. 2000 Page(s):453 - 458
Digital Object Identifier 10.1109/ICCD.2000.878322
[AbstractPlus](#) | Full Text: [PDF\(652 KB\)](#) IEEE CNF
- ☐ 34. **Real-time symbolic model checking for hard real-time systems**
Tachi, Y.; Yamane, S.;
Real-Time Computing Systems and Applications, 1999. RTCSA '99. Sixth Inte
Conference on
13-15 Dec. 1999 Page(s):496 - 499
Digital Object Identifier 10.1109/RTCSA.1999.811305
[AbstractPlus](#) | Full Text: [PDF\(264 KB\)](#) IEEE CNF
- ☐ 35. **Analysis and verification of multi-agent interaction protocols**
Wu Wen; Mizoguchi, F.;
Software Engineering Conference, 1999. (APSEC '99) Proceedings. Sixth Asia
7-10 Dec. 1999 Page(s):252 - 259
Digital Object Identifier 10.1109/APSEC.1999.809610
[AbstractPlus](#) | Full Text: [PDF\(580 KB\)](#) IEEE CNF
- ☐ 36. **Model checking the secure electronic transaction (SET) protocol**
Lu, S.; Smolka, S.A.;
Modeling, Analysis and Simulation of Computer and Telecommunication Syste
Proceedings. 7th International Symposium on
24-28 Oct. 1999 Page(s):358 - 364
Digital Object Identifier 10.1109/MASCOT.1999.805074
[AbstractPlus](#) | Full Text: [PDF\(72 KB\)](#) IEEE CNF
- ☐ 37. **Formal verification of a snoop-based cache coherence protocol using sy
checking**
Srinivasan, S.; Chhabra, P.S.; Jaini, P.K.; Aziz, A.; John, L.;
VLSI Design, 1999. Proceedings. Twelfth International Conference On
7-10 Jan. 1999 Page(s):288 - 293
Digital Object Identifier 10.1109/ICVD.1999.745162
[AbstractPlus](#) | Full Text: [PDF\(56 KB\)](#) IEEE CNF
- ☐ 38. **An approach to verify a large scale system-on-a-chip using symbolic mo**
Takayama, K.; Satoh, T.; Nakata, T.; Hirose, F.;
Computer Design: VLSI in Computers and Processors, 1998. ICCD '98. Proce
International Conference on
5-7 Oct. 1998 Page(s):308 - 313
Digital Object Identifier 10.1109/ICCD.1998.727066
[AbstractPlus](#) | Full Text: [PDF\(96 KB\)](#) IEEE CNF
- ☐ 39. **Approximate Reachability Don't Cares for CTL model checking**
In-Ho Moon; Jae-Young Jang; Hachtel, G.D.; Somenzi, F.; Jun Yuan; Pixley, C
Computer-Aided Design, 1998. ICCAD 98. Digest of Technical Papers. 1998 II
International Conference on
8-12 Nov 1998 Page(s):351 - 358
[AbstractPlus](#) | Full Text: [PDF\(892 KB\)](#) IEEE CNF
- ☐ 40. **Validation with guided search of the state space**
Yang, C.H.; Dill, D.L.;

Design Automation Conference, 1998. Proceedings
15-19 Jun 1998 Page(s):599 - 604

[AbstractPlus](#) | Full Text: [PDF\(596 KB\)](#) IEEE CNF

- ☐ **41. Formal verification of VHDL: the model checker CV**
Deharbe, D.; Shankar, S.; Clarke, E.M.;
Integrated Circuit Design, 1998. Proceedings. XI Brazilian Symposium on
30 Sept.-3 Oct. 1998 Page(s):95 - 98
Digital Object Identifier 10.1109/SBCCI.1998.715418
[AbstractPlus](#) | Full Text: [PDF\(64 KB\)](#) IEEE CNF

- ☐ **42. Forward model checking techniques oriented to buggy designs**
Iwashita, H.; Nakata, T.;
Computer-Aided Design, 1997. Digest of Technical Papers., 1997 IEEE/ACM I
Conference on
9-13 Nov. 1997 Page(s):400 - 404
Digital Object Identifier 10.1109/ICCAD.1997.643567
[AbstractPlus](#) | Full Text: [PDF\(420 KB\)](#) IEEE CNF

- ☐ **43. Analyzing a PowerPCTM 620 microprocessor silicon failure using model**
Raimi, R.; Lear, J.;
Test Conference, 1997. Proceedings., International
1-6 Nov. 1997 Page(s):964 - 973
Digital Object Identifier 10.1109/TEST.1997.639712
[AbstractPlus](#) | Full Text: [PDF\(784 KB\)](#) IEEE CNF

- ☐ **44. Quantitative analysis and model checking**
Huth, M.; Kwiatkowska, M.;
Logic in Computer Science, 1997. LICS '97. Proceedings., 12th Annual IEEE S
29 June-2 July 1997 Page(s):111 - 122
Digital Object Identifier 10.1109/LICS.1997.614940
[AbstractPlus](#) | Full Text: [PDF\(872 KB\)](#) IEEE CNF

- ☐ **45. Word level model checking-avoiding the Pentium FDIV error**
Clarke, E.M.; Khaira, M.; Zhao, X.;
Design Automation Conference Proceedings 1996, 33rd
3-7 June 1996 Page(s):645 - 648
[AbstractPlus](#) | Full Text: [PDF\(368 KB\)](#) IEEE CNF

- ☐ **46. An approach to hierarchy model checking via evaluating CTL hierarchica**
Zuan Zhang;
Test Symposium, 1995., Proceedings of the Fourth Asian
23-24 Nov. 1995 Page(s):45 - 49
Digital Object Identifier 10.1109/ATS.1995.485315
[AbstractPlus](#) | Full Text: [PDF\(384 KB\)](#) IEEE CNF

- ☐ **47. Model-checking for real-time systems**
Alur, R.; Courcoubetis, C.; Dill, D.;
Logic in Computer Science, 1990. LICS '90, Proceedings., Fifth Annual IEEE S
4-7 June 1990 Page(s):414 - 425
Digital Object Identifier 10.1109/LICS.1990.113766
[AbstractPlus](#) | Full Text: [PDF\(956 KB\)](#) IEEE CNF

- ☐ **48. Automatic verification of finite-state concurrent systems**
Clarke, E.M., Jr.;
Logic in Computer Science, 1994. LICS '94. Proceedings., Symposium on
4-7 July 1994 Page(s):126
Digital Object Identifier 10.1109/LICS.1994.316079
[AbstractPlus](#) | Full Text: [PDF\(52 KB\)](#) IEEE CNF

- ☐ **49. State-based model checking of event-driven system requirements**

Atlee, J.M.; Gannon, J.;
Software Engineering, IEEE Transactions on
Volume 19, Issue 1, Jan. 1993 Page(s):24 - 40
Digital Object Identifier 10.1109/32.210305
[AbstractPlus](#) | Full Text: [PDF](#)(1340 KB) IEEE JNL

- ☐ **50. Communication space reduction for formal verification of secure authentication protocols**
Kyoil Kim; Abraham, J.A.;
Advanced Issues of E-Commerce and Web-Based Information Systems, WEC International Workshop on.
21-22 June 2001 Page(s):225 - 227
Digital Object Identifier 10.1109/WECWIS.2001.933928
[AbstractPlus](#) | Full Text: [PDF](#)(328 KB) IEEE CNF
- ☐ **51. Coverage based test-case generation using model checkers**
Rayadurgam, S.; Heimdahl, M.P.E.;
Engineering of Computer Based Systems, 2001. ECBS 2001. Proceedings. Eighth International Conference and Workshop on the
17-20 April 2001 Page(s):83 - 91
Digital Object Identifier 10.1109/ECBS.2001.922409
[AbstractPlus](#) | Full Text: [PDF](#)(892 KB) IEEE CNF
- ☐ **52. Model checking of S3C2400X industrial embedded SOC product**
Hoon Choi; Byeongwhae Yun; Yuntae Lee; Hyunglae Roh;
Design Automation Conference, 2001. Proceedings
2001 Page(s):611 - 616
[AbstractPlus](#) | Full Text: [PDF](#)(676 KB) IEEE CNF
- ☐ **53. Formal verification of Statecharts using finite-state model checkers**
Qianchuan Zhao; Krogh, B.H.;
American Control Conference, 2001. Proceedings of the 2001
Volume 1, 25-27 June 2001 Page(s):313 - 318 vol.1
Digital Object Identifier 10.1109/ACC.2001.945563
[AbstractPlus](#) | Full Text: [PDF](#)(456 KB) IEEE CNF
- ☐ **54. Model checking UML statecharts**
Wei Dong; Ji Wang; Xuan Qi; Zhi-Chang Qi;
Software Engineering Conference, 2001. APSEC 2001. Eighth Asia-Pacific
4-7 Dec. 2001 Page(s):363 - 370
[AbstractPlus](#) | Full Text: [PDF](#)(803 KB) IEEE CNF
- ☐ **55. Model checking for an executable subset of UML**
Fei Xie; Levin, V.; Browne, J.C.;
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annual Conference on
26-29 Nov. 2001 Page(s):333 - 336
[AbstractPlus](#) | Full Text: [PDF](#)(467 KB) IEEE CNF
- ☐ **56. The Production Cell: an exercise in the formal verification of a UML model**
Lilius, J.; Palter, I.P.;
System Sciences, 2000. Proceedings of the 33rd Annual Hawaii International Conference on
Jan 4-7 2000 Page(s):10 pp. vol.1
[AbstractPlus](#) | Full Text: [PDF](#)(124 KB) IEEE CNF
- ☐ **57. Using model checking for verification of partitioning properties in integrated avionics**
Cofer, D.; Engstrom, E.; Weininger, N.; Penix, J.; Visser, W.;
Digital Avionics Systems Conferences, 2000. Proceedings. DASC. The 19th
Volume 1, 7-13 Oct. 2000 Page(s):1D2/1 - 1D210 vol.1
Digital Object Identifier 10.1109/DASC.2000.886889
[AbstractPlus](#) | Full Text: [PDF](#)(732 KB) IEEE CNF

- ☐ **58. Tabled resolution + constraints: a recipe for model checking real-time sy**
Xiaoqun Du; Ramakrishnan, C.R.; Smolka, S.A.;
Real-Time Systems Symposium, 2000. Proceedings. The 21st IEEE
27-30 Nov. 2000 Page(s):175 - 184
Digital Object Identifier 10.1109/REAL.2000.896007
[AbstractPlus](#) | Full Text: [PDF\(820 KB\)](#) IEEE CNF
- ☐ **59. Model checking programs**
Visser, W.; Havelund, K.; Brat, G.; Seungjoon Park;
Automated Software Engineering, 2000. Proceedings ASE 2000. The Fifteenth
International Conference on
11-15 Sept. 2000 Page(s):3 - 11
Digital Object Identifier 10.1109/ASE.2000.873645
[AbstractPlus](#) | Full Text: [PDF\(836 KB\)](#) IEEE CNF
- ☐ **60. Java model checking**
Park, D.Y.W.; Stern, U.; Skakkebaek, J.U.; Dill, D.L.;
Automated Software Engineering, 2000. Proceedings ASE 2000. The Fifteenth
International Conference on
11-15 Sept. 2000 Page(s):253 - 256
Digital Object Identifier 10.1109/ASE.2000.873671
[AbstractPlus](#) | Full Text: [PDF\(320 KB\)](#) IEEE CNF
- ☐ **61. Simulation strategy after model checking: experience in industrial SOC d**
Hoon Choi; Byeong-Whee Yun; Yun-Tae Lee;
High-Level Design Validation and Test Workshop, 2000. Proceedings. IEEE In
8-10 Nov. 2000 Page(s):77 - 79
Digital Object Identifier 10.1109/HLDVT.2000.889563
[AbstractPlus](#) | Full Text: [PDF\(264 KB\)](#) IEEE CNF
- ☐ **62. Automated translation of UML models of architectures for verification an
using SPIN**
Bose, P.;
Automated Software Engineering, 1999. 14th IEEE International Conference o
12-15 Oct. 1999 Page(s):102 - 109
Digital Object Identifier 10.1109/ASE.1999.802135
[AbstractPlus](#) | Full Text: [PDF\(76 KB\)](#) IEEE CNF
- ☐ **63. Hazard checking in pipelined processor designs using symbolic model c**
Schonherr, J.; Schreiber, I.; Fordran, E.; Straube, B.;
EUROMICRO Conference, 1999. Proceedings. 25th
Volume 1, 8-10 Sept. 1999 Page(s):75 - 78 vol.1
Digital Object Identifier 10.1109/EURMIC.1999.794450
[AbstractPlus](#) | Full Text: [PDF\(60 KB\)](#) IEEE CNF
- ☐ **64. Increasing efficiency of symbolic model checking by accelerating dynam
reordering**
Meinel, C.; Stangier, C.;
Design, Automation and Test in Europe Conference and Exhibition 1999. Proc
9-12 March 1999 Page(s):760 - 761
Digital Object Identifier 10.1109/DATE.1999.761218
[AbstractPlus](#) | Full Text: [PDF\(24 KB\)](#) IEEE CNF
- ☐ **65. Decoupling synchronization from local control for efficient symbolic mor
statecharts**
Chan, W.; Anderson, R.J.; Beame, P.; Jones, D.H.; Notkin, D.; Warner, W.E.;
Software Engineering, 1999. Proceedings of the 1999 International Conferenc
16-22 May 1999 Page(s):142 - 151
Digital Object Identifier 10.1109/ICSE.1999.841003
[AbstractPlus](#) | Full Text: [PDF\(972 KB\)](#) IEEE CNF

- ☐ **66. Using the ASTRAL model checker to analyze Mobile IP**
Zhe Dang; Kemmerer, R.A.;
Software Engineering, 1999. Proceedings of the 1999 International Conference
16-22 May 1999 Page(s):132 - 141
Digital Object Identifier 10.1109/ICSE.1999.841002
[AbstractPlus](#) | Full Text: [PDF\(860 KB\)](#) IEEE CNF
- ☐ **67. Applying model checking to concurrent object-oriented software**
Seung Mo Cho; Doo Hwan Bae; Sung Deok Cha; Young Gon Kim; Byung Kyu Kim; Kr, Y.K.A.; Kim, S.T.;
Autonomous Decentralized Systems, 1999. Integration of Heterogeneous Syst
Proceedings. The Fourth International Symposium on
21-23 March 1999 Page(s):380 - 383
Digital Object Identifier 10.1109/ISADS.1999.838464
[AbstractPlus](#) | Full Text: [PDF\(156 KB\)](#) IEEE CNF
- ☐ **68. A partial approach to model checking**
Godefroid, P.; Wolper, P.;
Logic in Computer Science, 1991. LICS '91., Proceedings of Sixth Annual IEEE
15-18 July 1991 Page(s):406 - 415
Digital Object Identifier 10.1109/LICS.1991.151664
[AbstractPlus](#) | Full Text: [PDF\(748 KB\)](#) IEEE CNF
- ☐ **69. Fast parallel algorithms for model checking using BDDs**
Lee, I.; Rajasekaran, S.;
Parallel Processing Symposium, 1993., Proceedings of Seventh International
13-16 April 1993 Page(s):444 - 448
Digital Object Identifier 10.1109/IPPS.1993.262834
[AbstractPlus](#) | Full Text: [PDF\(380 KB\)](#) IEEE CNF
- ☐ **70. Compositional model checking of Ada tasking programs**
Fischer, J.; Gerber, R.;
Computer Assurance, 1994. COMPASS '94 'Safety, Reliability, Fault Toleranc
and Real Time, Security'. Proceedings of the Ninth Annual Conference on
27 June-1 July 1994 Page(s):135 - 147
Digital Object Identifier 10.1109/COMPASS.1994.318460
[AbstractPlus](#) | Full Text: [PDF\(800 KB\)](#) IEEE CNF
- ☐ **71. Symbolic model checking for sequential circuit verification**
Burch, J.R.; Clarke, E.M.; Long, D.E.; McMillan, K.L.; Dill, D.L.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction
Volume 13, Issue 4, April 1994 Page(s):401 - 424
Digital Object Identifier 10.1109/43.275352
[AbstractPlus](#) | Full Text: [PDF\(2324 KB\)](#) IEEE JNL
- ☐ **72. Application of parametric model checking - the Root Contention protocol**
Bandini, G.; Spelberg, R.L.; de Rooij, R.C.H.; Toetenel, W.J.;
System Sciences, 2001. Proceedings of the 34th Annual Hawaii International (C
Jan 3-6 2001 Page(s):10 pp.
[AbstractPlus](#) | Full Text: [PDF\(220 KB\)](#) IEEE CNF
- ☐ **73. Formal verification of embedded real-time software in component-based frameworks**
Pao-Ann Hsiung; Win-Bin See; Trong-Yen Lee; Jih-Ming Fu; Sao-Jie Chen;
Software Engineering Conference, 2001. APSEC 2001. Eighth Asia-Pacific
4-7 Dec. 2001 Page(s):71 - 78
[AbstractPlus](#) | Full Text: [PDF\(861 KB\)](#) IEEE CNF
- ☐ **74. Three approximation techniques for ASTRAL symbolic model checking of real-time systems**
Zhe Dang; Kemmerer, R.A.;
Software Engineering, 2000. Proceedings of the 2000 International Conference

4-11 June 2000 Page(s):345 - 354

Digital Object Identifier 10.1109/ICSE.2000.870425

[AbstractPlus](#) | Full Text: [PDF](#)(1036 KB) IEEE CNF

- ☐ **75. Freedom, weakness, and determinism: from linear-time to branching-time**
Kupferman, O.; Vardit, M.Y.;
Logic in Computer Science, 1998. Proceedings. Thirteenth Annual IEEE Symp
21-24 June 1998 Page(s):81 - 92
Digital Object Identifier 10.1109/LICS.1998.705645
[AbstractPlus](#) | Full Text: [PDF](#)(252 KB) IEEE CNF
- ☐ **76. An overview and application of model reduction techniques in formal ver**
Baumgartner, J.; Heyman, T.;
Performance, Computing and Communications, 1998. IPCCC '98., IEEE Interr
16-18 Feb. 1998 Page(s):165 - 171
Digital Object Identifier 10.1109/PCCC.1998.659939
[AbstractPlus](#) | Full Text: [PDF](#)(616 KB) IEEE CNF
- ☐ **77. Incremental CTL model checking using BDD subsetting**
Pardo, A.; Hachtel, G.D.;
Design Automation Conference, 1998. Proceedings
15-19 Jun 1998 Page(s):457 - 462
[AbstractPlus](#) | Full Text: [PDF](#)(640 KB) IEEE CNF
- ☐ **78. Partial model checking**
Andersen, H.R.;
Logic in Computer Science, 1995. LICS '95. Proceedings., Tenth Annual IEEE
26-29 June 1995 Page(s):398 - 407
Digital Object Identifier 10.1109/LICS.1995.523274
[AbstractPlus](#) | Full Text: [PDF](#)(668 KB) IEEE CNF
- ☐ **79. Automated modular specification and verification of real-time reactive sy**
Ostroff, J.S.;
Industrial-Strength Formal Specification Techniques, 1995. Proceedings., Wor
5-8 April 1995 Page(s):108 - 121
Digital Object Identifier 10.1109/WIFT.1995.515483
[AbstractPlus](#) | Full Text: [PDF](#)(1944 KB) IEEE CNF
- ☐ **80. Compositional and symbolic model-checking of real-time systems**
Larsen, K.G.; Pettersson, P.; Wang Yi;
Real-Time Systems Symposium, 1995. Proceedings., 16th IEEE
5-7 Dec. 1995 Page(s):76 - 87
Digital Object Identifier 10.1109/REAL.1995.495198
[AbstractPlus](#) | Full Text: [PDF](#)(988 KB) IEEE CNF
- ☐ **81. Bug identification of a real chip design by symbolic model checking**
Chen, B.; Yamazaki, M.; Fujita, M.;
European Design and Test Conference, 1994. EDAC, The European Conferer
Automation. ETC European Test Conference. EUROASIC, The European Eve
Design, Proceedings.
28 Feb.-3 March 1994 Page(s):132 - 136
Digital Object Identifier 10.1109/EDTC.1994.326886
[AbstractPlus](#) | Full Text: [PDF](#)(356 KB) IEEE CNF
- ☐ **82. Modeling programmable logic controllers for logic verification**
Moon, I.;
Control Systems Magazine, IEEE
Volume 14, Issue 2, April 1994 Page(s):53 - 59
Digital Object Identifier 10.1109/37.272781
[AbstractPlus](#) | Full Text: [PDF](#)(520 KB) IEEE JNL

- ☐ **83. Model checking in practice: the T9000 virtual channel processor**
Barrett, G.;
Software Engineering, IEEE Transactions on
Volume 21, Issue 2, Feb. 1995 Page(s):69 - 78
Digital Object Identifier 10.1109/32.345823
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(816 KB\)](#) IEEE JNL
- ☐ **84. E-process design and assurance using model checking**
Wang, W.; Hidvegi, Z.; Bailey, A.D., Jr.; Whinston, A.B.;
Computer
Volume 33, Issue 10, Oct. 2000 Page(s):48 - 53
Digital Object Identifier 10.1109/2.876292
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(212 KB\)](#) IEEE JNL
- ☐ **85. Automatic support for verification of secure transactions in distributed e using symbolic model checking**
Di Sciascio, E.; Donini, F.M.; Mongiello, M.; Piscitelli, G.;
Information Technology Interfaces, 2001. ITI 2001. Proceedings of the 23rd Int
Conference on
19-22 June 2001 Page(s):447 - 454 vol.1
Digital Object Identifier 10.1109/ITI.2001.938054
[AbstractPlus](#) | Full Text: [PDF\(616 KB\)](#) IEEE CNF
- ☐ **86. Using a model checker to test safety properties**
Ammann, P.; Wei Ding; Daling Xu;
Engineering of Complex Computer Systems, 2001. Proceedings. Seventh IEE
Conference on
11-13 June 2001 Page(s):212 - 221
Digital Object Identifier 10.1109/ICECCS.2001.930180
[AbstractPlus](#) | Full Text: [PDF\(728 KB\)](#) IEEE CNF
- ☐ **87. Events and constraints: a graphical editor for capturing logic requiremen**
Smith, M.H.; Holzmann, G.J.; Etessami, K.;
Requirements Engineering, 2001. Proceedings. Fifth IEEE International Sympo
27-31 Aug. 2001 Page(s):14 - 22
Digital Object Identifier 10.1109/ISRE.2001.948539
[AbstractPlus](#) | Full Text: [PDF\(712 KB\)](#) IEEE CNF
- ☐ **88. Introduction to generalized symbolic trajectory evaluation**
Jin Yang; Seger, C.-J.H.;
Computer Design, 2001. ICCD 2001. Proceedings. 2001 International Confere
23-26 Sept. 2001 Page(s):360 - 365
Digital Object Identifier 10.1109/ICCD.2001.955052
[AbstractPlus](#) | Full Text: [PDF\(496 KB\)](#) IEEE CNF
- ☐ **89. Automatic synthesis of the DC specifications of lip synchronisation prot**
Huadong Ma; Liang Li; Jianzhong Wang; Naijun Zhan;
Software Engineering Conference, 2001. APSEC 2001. Eighth Asia-Pacific
4-7 Dec. 2001 Page(s):371 - 378
[AbstractPlus](#) | Full Text: [PDF\(676 KB\)](#) IEEE CNF
- ☐ **90. An automated tool for analyzing Petri nets using Spin**
Gannod, G.C.; Gupta, S.;
Automated Software Engineering, 2001. (ASE 2001). Proceedings. 16th Annu
Conference on
26-29 Nov. 2001 Page(s):404 - 407
[AbstractPlus](#) | Full Text: [PDF\(468 KB\)](#) IEEE CNF
- ☐ **91. Test-sequence generation from formal requirement models**
Rayadurgam, S.; Heimdahl, M.P.E.;
High Assurance Systems Engineering, 2001. Sixth IEEE International Sympos
22-24 Oct. 2001 Page(s):23 - 31

Digital Object Identifier 10.1109/HASE.2001.966804

[AbstractPlus](#) | Full Text: [PDF](#)(192 KB) IEEE CNF

- ☐ **92. Using a hardware model checker to verify software**
Edwards, S.A.; Ma, T.; Damiano, R.;
ASIC, 2001. Proceedings. 4th International Conference on
23-25 Oct. 2001 Page(s):85 - 90
Digital Object Identifier 10.1109/ICASIC.2001.982504
[AbstractPlus](#) | Full Text: [PDF](#)(599 KB) IEEE CNF

- ☐ **93. Verifying sequential behavior with model checking**
Biere, A.;
ASIC, 2001. Proceedings. 4th International Conference on
23-25 Oct. 2001 Page(s):29 - 32
Digital Object Identifier 10.1109/ICASIC.2001.982490
[AbstractPlus](#) | Full Text: [PDF](#)(438 KB) IEEE CNF

- ☐ **94. A model checking approach to evaluating system level dynamic power m
policies for embedded systems**
Shukla, S.K.; Gupta, R.K.;
High-Level Design Validation and Test Workshop, 2001. Proceedings. Sixth IE
7-9 Nov. 2001 Page(s):53 - 57
Digital Object Identifier 10.1109/HLDVT.2001.972807
[AbstractPlus](#) | Full Text: [PDF](#)(75 KB) IEEE CNF

- ☐ **95. Practical use of sequential ATPG for model checking: going the extra mil**
Hsiao, M.; Jain, J.;
High-Level Design Validation and Test Workshop, 2001. Proceedings. Sixth IE
7-9 Nov. 2001 Page(s):39 - 44
Digital Object Identifier 10.1109/HLDVT.2001.972805
[AbstractPlus](#) | Full Text: [PDF](#)(152 KB) IEEE CNF

- ☐ **96. Iterative abstraction-based CTL model checking**
Jae-Young Jang; In-Ho Moon; Hachtel, G.D.;
Design, Automation and Test in Europe Conference and Exhibition 2000. Proc
27-30 March 2000 Page(s):502 - 507
Digital Object Identifier 10.1109/DATE.2000.840831
[AbstractPlus](#) | Full Text: [PDF](#)(208 KB) IEEE CNF

- ☐ **97. Analysis of real-time code by model checking**
Guaspari, D.; Naydich, D.;
Digital Avionics Systems Conferences, 2000. Proceedings. DASC. The 19th
Volume 1, 7-13 Oct. 2000 Page(s):1D5/1 - 1D5/8 vol.1
Digital Object Identifier 10.1109/DASC.2000.886892
[AbstractPlus](#) | Full Text: [PDF](#)(572 KB) IEEE CNF

- ☐ **98. Implicit model checking: formal verification technique for large-scale dis**
Taeshin Park;
Computer-Aided Control System Design, 2000. CACSD 2000. IEEE Internati
on
25-27 Sept. 2000 Page(s):135 - 140
Digital Object Identifier 10.1109/CACSD.2000.900200
[AbstractPlus](#) | Full Text: [PDF](#)(456 KB) IEEE CNF

- ☐ **99. Validating executable controller specifications through formal model che**
Scillieri, J.J.; Butts, K.R.; Freudenberg, J.S.;
Computer-Aided Control System Design, 2000. CACSD 2000. IEEE Internati
on
25-27 Sept. 2000 Page(s):179 - 184
Digital Object Identifier 10.1109/CACSD.2000.900208
[AbstractPlus](#) | Full Text: [PDF](#)(456 KB) IEEE CNF

☐ 100. Verification of logic control design using SIPN and model checking: met study


Weng, X.; Litz, L.;

American Control Conference, 2000. Proceedings of the 2000

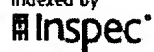
Volume 6, 28-30 June 2000 Page(s):4072 - 4076 vol.6

Digital Object Identifier 10.1109/ACC.2000.876987

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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 Vicario, E.;
 Software Engineering, IEEE Transactions on
 Volume 27, Issue 8, Aug. 2001 Page(s):728 - 748
 Digital Object Identifier 10.1109/32.940727
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(1496 KB) IEEE JNL
- ☐ 2. **An efficient state space generation for the analysis of real-time systems**
 Kang, I.; Lee, I.; Young-Si Kim;
 Software Engineering, IEEE Transactions on
 Volume 26, Issue 5, May 2000 Page(s):453 - 477
 Digital Object Identifier 10.1109/32.846302
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(576 KB) IEEE JNL
- ☐ 3. **Validation with guided search of the state space**
 Yang, C.H.; Dill, D.L.;
 Design Automation Conference, 1998. Proceedings
 15-19 Jun 1998 Page(s):599 - 604
[AbstractPlus](#) | Full Text: [PDF](#)(596 KB) IEEE CNF
- ☐ 4. **On the use of a formal RE language-the generalized railroad crossing pro**
 Du Bois, P.; Dubois, E.; Zeppen, J.-M.;
 Requirements Engineering, 1997., Proceedings of the Third IEEE International
 6-10 Jan. 1997 Page(s):128 - 137
 Digital Object Identifier 10.1109/ISRE.1997.566850
[AbstractPlus](#) | Full Text: [PDF](#)(844 KB) IEEE CNF


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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard



1. Fundamental CAD algorithms

Breuer, M.A.; Sarrafzadeh, M.; Somenzi, F.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction:
Volume 19, Issue 12, Dec. 2000 Page(s):1449 - 1475
Digital Object Identifier 10.1109/43.898826

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(484 KB\)](#) IEEE JNL

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐
- 1.
- An analysis of fault partitioned parallel test generation**
-
- Wolf, J.M.; Kaufman, L.M.; Klenke, R.H.; Aylor, J.H.; Waxman, R.;
-
- Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction:
-
- Volume 15, Issue 5, May 1996 Page(s):517 - 534
-
- Digital Object Identifier 10.1109/43.506139
-
- [AbstractPlus](#)
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1 [Hyperdocuments as automata: verification of trace-based browsing properties by model checking](#)

P. David Stotts, Richard Furuta, Cyrano Ruiz Cabarrus

January 1998 **ACM Transactions on Information Systems (TOIS)**, Volume 16 Issue 1Full text available: [pdf\(474.20 KB\)](#)
 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present a view of hyperdocuments in which each document encodes its own browsing semantics in its links. This requires a mental shift in how a hyperdocument is thought of abstractly. Instead of treating the links of a document as defining a static directed graph, they are thought of as defining an abstract program, termed the links-automaton of the document. A branching temporal logic notation, termed HTL*, is introduced for specifying properties a document should exhibit ...

Keywords: Petri nets, browsing semantics, hypermedia, hypertext, model checking, temporal logic

2 [Verifying security protocols with Brutus](#)

E. M. Clarke, S. Jha, W. Marrero

October 2000 **ACM Transactions on Software Engineering and Methodology (TOSEM)**, Volume 9 Issue 4Full text available: [pdf\(347.12 KB\)](#)
 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Due to the rapid growth of the "Internet" and the "World Wide Web" security has become a very important concern in the design and implementation of software systems. Since security has become an important issue, the number of protocols in this domain has become very large. These protocols are very diverse in nature. If a software architect wants to deploy some of these protocols in a system, they have to be sure that the protocol has the right properties as dictated ...

Keywords: authentication and secure payment protocols, formal methods, model-checking

3 [A compiler for analyzing cryptographic protocols using noninterference](#)

Antonio Durante, Riccardo Focardi, Roberto Gorrieri

October 2000 **ACM Transactions on Software Engineering and Methodology (TOSEM)**, Volume 9 Issue 4Full text available: [pdf\(291.90 KB\)](#)
 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

terms


The Security Process Algebra (SPA) is a CCS-like specification language where actions belong to two different levels of confidentiality. It has been used to define several noninterference-like security properties whose verification has been automated by the tool CoSeC. In recent years, a method for analyzing security protocols using SPA and CoSeC has been developed. Even if it has been useful in analyzing small security protocols, this method has shown to be error-prone, as it requires the ...

Keywords: automatic verification, cryptographic protocols, noninterference, process algebra, verification tool

4 A practical method for verifying event-driven software

Gerard J. Holzmann, Margaret H. Smith

May 1999 **Proceedings of the 21st international conference on Software engineering**

Full text available:  [pdf\(1.40 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: case studies, feature interactive, formal methods, model checking, reactive systems, software testing, software verification, telephone call processing

5 Composite model-checking: verification with type-specific symbolic representations

Tevfik Bultan, Richard Gerber, Christopher League

January 2000 **ACM Transactions on Software Engineering and Methodology (TOSEM)**,

Volume 9 Issue 1

Full text available:  [pdf\(400.17 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


There has been a surge of progress in automated verification methods based on state exploration. In areas like hardware design, these technologies are rapidly augmenting key phases of testing and validation. To date, one of the most successful of these methods has been symbolic model-checking, in which large finite-state machines are encoded into compact data structures such as Binary Decision Diagrams (BDDs), and are then checked for safety and liveness properties. However, these technique ...

Keywords: Presburger arithmetic, binary decision diagrams, symbolic model-checking

6 Data flow analysis is model checking of abstract interpretations

David A. Schmidt

January 1998 **Proceedings of the 25th ACM SIGPLAN-SIGACT symposium on Principles of programming languages**

Full text available:  [pdf\(1.42 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

7 Bandera: extracting finite-state models from Java source code

James C. Corbett, Matthew B. Dwyer, John Hatcliff, Shawn Laubach, Corina S. Păsăreanu, Robby, Hongjun Zheng

June 2000 **Proceedings of the 22nd international conference on Software engineering**

Full text available:  [pdf\(345.15 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Finite-state verification techniques, such as model checking, have shown promise as a cost-effective means for finding defects in hardware designs. To date, the application of these techniques to software has been hindered by several obstacles. Chief among these is the problem of constructing a finite-state model that approximates the executable behavior of the software system of interest. Current best-practice involves hand-construction of models


which is expensive (prohibitive for all but ...

Keywords: abstract interpretation, model checking, model extraction, program specialization, program verification, slicing

8 Verification techniques for cache coherence protocols

Fong Pong, Michel Dubois

March 1997 **ACM Computing Surveys (CSUR)**, Volume 29 Issue 1.

Full text available:  pdf(1.25 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

In this article we present a comprehensive survey of various approaches for the verification of cache coherence protocols based on state enumeration, (symbolic model checking, and symbolic state models. Since these techniques search the state space of the protocol exhaustively, the amount of memory required to manipulate that state information and the verification time grow very fast with the number of processors and the complexity of the protocol mechanism ...

Keywords: cache coherence, finite state machine, protocol verification, shared-memory multiprocessors, state representation and expansion

9 Using shape analysis to reduce finite-state models of concurrent Java programs

James C. Corbett

January 2000 **ACM Transactions on Software Engineering and Methodology (TOSEM)**, Volume 9 Issue 1

Full text available:  pdf(284.92 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Finite-state verification (e.g., model checking) provides a powerful means to detect concurrency errors, which are often subtle and difficult to reproduce. Nevertheless, widespread use of this technology by developers is unlikely until tools provide automated support for extracting the required finite-state models directly from program source. Unfortunately, the dynamic features of modern languages such as Java complicate the construction of compact finite-state models for verification. I ...

Keywords: Java, concurrent systems, finite-state verification, model extraction, modeling, shape analysis, state-space reductions

10 Modular refinement of hierarchic reactive machines

Rajeev Alur, Radu Grosu

January 2000 **Proceedings of the 27th ACM SIGPLAN-SIGACT symposium on Principles of programming languages**

Full text available:  pdf(1.62 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Scalable formal analysis of reactive programs demands integration of modular reasoning techniques with existing analysis tools. Principles such as abstraction, compositional refinement, and assume-guarantee reasoning are well understood for architectural hierarchy that describes the communication structure between component processes, and have been shown to be useful. In this paper, we develop the theory of modular reasoning for behavior hierarchy that describes control str ...

11 Formal verification in hardware design: a survey

Christoph Kern, Mark R. Greenstreet

April 1999 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 4 Issue 2

Full text available:  pdf(411.53 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

In recent years, formal methods have emerged as an alternative approach to ensuring the quality and correctness of hardware designs, overcoming some of the limitations of traditional validation techniques such as simulation and testing. There are two main aspects to the application of formal methods in a design process: the formal framework used to specify desired properties of a design and the verification techniques and tools used to reason about the relationship between a spec ...

Keywords: case studies, formal methods, formal verification, hardware verification, language containment, model checking, survey, theorem proving

12 HSIS: a BDD-based environment for formal verification

A. Aziz, F. Balarin, S.-T. Cheng, R. Hojati, T. Kam, S. C. Krishnan, R. K. Ranjan, T. R. Shiple, V. Singhal, S. Tasiran, H.-Y. Wang, R. K. Brayton, A. L. Sangiovanni-Vincentelli

June 1994 **Proceedings of the 31st annual conference on Design automation**

Full text available:  pdf(91.11 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

13 Model checking of hierarchical state machines

Rajeev Alur, Mihalis Yannakakis

November 1998 **ACM SIGSOFT Software Engineering Notes , Proceedings of the 6th ACM SIGSOFT international symposium on Foundations of software engineering**, Volume 23 Issue 6

Full text available:  pdf(1.09 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Model checking is emerging as a practical tool for detecting logical errors in early stages of system design. We investigate the model checking of hierarchical (nested) systems, i.e. finite state machines whose states themselves can be other machines. This nesting ability is common in various software design methodologies and is available in several commercial modeling tools. The straightforward way to analyze a hierarchical machine is to flatten it (thus, incurring an exponential blow up) and a ...

14 Requirements engineering in the year 00: a research perspective

Axel van Lamsweerde

June 2000 **Proceedings of the 22nd international conference on Software engineering**

Full text available:  pdf(205.79 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Requirements engineering (RE) is concerned with the identification of the goals to be achieved by the envisioned system, the operationalization of such goals into services and constraints, and the assignment of responsibilities for the resulting requirements to agents such as humans, devices, and software. The processes involved in RE include domain analysis, elicitation, specification, assessment, negotiation, documentation, and evolution. Getting high-quality requirements is difficult and ...

15 Strategic directions in concurrency research

Rance Cleaveland, Scott A. Smolka

December 1996 **ACM Computing Surveys (CSUR)**, Volume 28 Issue 4

Full text available:  pdf(323.67 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

16 Compositional verification of concurrent systems using Petri-net-based condensation rules

Eric Y. T. Juan, Jeffrey J. P. Tsai, Tadao Murata

September 1998 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 20 Issue 5

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)

Full text available:  [pdf\(578.81 KB\)](#)[terms](#)

The state-explosion problem of formal verification has obstructed its application to large-scale software systems. In this article, we introduce a set of new condensation theories: IOT-failure equivalence, IOT-state equivalence, and firing-dependence theory to cope with this problem. Our condensation theories are much weaker than current theories used for the compositional verification of Petri nets. More significantly, our new condensation theories can eliminate the interleaved behaviors ...



Keywords: Petri nets, boundedness, compositional verification, deadlock states, reachability analysis, reachability graphs, reachable markings

17 Formal verification of FIRE: a case study

Jae-Young Jang, Shaz Qadeer, Matt Kaufmann, Carl Pixley

June 1997 **Proceedings of the 34th annual conference on Design automation - Volume 00**

Full text available:

 [pdf\(93.19 KB\)](#)
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We present our experiences with the formal verification of an automotive chip used to control the safety features in a car. We used a BDD based model checker in our work. We describe our verification methodology for verifying a very complicated property on a relatively large design. We also describe the bugs that were found and present our views on how to make model checking an effective integrated part of the design flow for complex hardware systems.

18 Using predicate abstraction to reduce object-oriented programs for model checking

William Visser, SeungJoon Park, John Penix

August 2000 **Proceedings of the third workshop on Formal methods in software practice**

Full text available:  [pdf\(385.20 KB\)](#)

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While it is becoming more common to see model checking applied to software requirements specifications, it is seldom applied to software implementations. The Automated Software Engineering group at NASA Ames is currently investigating the use of model checking for actual source code, with the eventual goal of allowing software developers to augment traditional testing with model checking. Because model checking suffers from the state-explosion problem, one of the main hurdles for program ...

Keywords: browsing

19 Symbolic model checking for event-driven real-time systems

Jin Yang, Aloysius K. Mok, Farn Wang

March 1997 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 19 Issue 2

Full text available:  [pdf\(562.52 KB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: binary decision diagrams

20 Checking safety properties using compositional reachability analysis

Shing Chi Cheung, Jeff Kramer

January 1999 **ACM Transactions on Software Engineering and Methodology (TOSEM)**, Volume 8 Issue 1

Full text available:  [pdf\(425.29 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The software architecture of a distributed program can be represented by a hierarchical composition of subsystems, with interacting processes at the leaves of the hierarchy. Compositional reachability analysis (CRA) is a promising state reduction technique which can be automated and used in stages to derive the overall behavior of a distributed program based on its architecture. CRA is particularly suitable for the analysis of programs that are subject to evolutionary change. When a-progra ...

Keywords: compositional reachability analysis, distributed systems, model checking, safety properties, static analysis

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5 Ushell: an environment for introduction to logic programming



L. Ümit Yalçınalp

March 1993 **Proceedings of the 1993 ACM/SIGAPP symposium on Applied computing: states of the art and practice**

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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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Systems, Man and Cybernetics, IEEE Transactions on
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- ☐ 2. **HOPE: an efficient parallel fault simulator for synchronous sequential cir**
Hyung Ki Lee; Dong Sam Ha;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction:
Volume 15, Issue 9, Sept. 1996 Page(s):1048 - 1058
Digital Object Identifier 10.1109/43.536711
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- ☐ 3. **An iterative rounding 2-approximation algorithm for the element connecti**
Fleischer, L.; Jain, K.; Williamson, D.P.;
Foundations of Computer Science, 2001. Proceedings. 42nd IEEE Symposium
8-11 Oct. 2001 Page(s):339 - 347
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[User Guide to the CtCoq Proof Environment - Bertot, Bertot, Coscoy, Goguen, ... \(1997\)](#) (Correct) (9 citations)

. 10 3.4 **Multiple Proofs** in Progress .

. 11 3.4.2 Shortcomings of the **Multiple Proofs** Mechanism . 12 3.5 Error

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[Hardness of Approximate Hypergraph Coloring - Guruswami, Håstad, Sudan \(2002\)](#) (Correct) (8 citations)

verifier accepts a given input, here we allow **multiple proofs** to be provided to the verifier. We say that a
 of a single proof. However when faced with **multiple proofs**, the intermediate tables we consider, such
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[Extension Table Built-ins for Prolog - Fan, Dietrich \(1992\)](#) (Correct) (7 citations)

are found. Since the ET* algorithm ignores **multiple proofs**, the answers found by the ET* algorithm
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[Verbalization of High-Level Formal Proofs - Holland-Minkley, Barzilay, al. \(1999\)](#) (Correct) (2 citations)

for every step but aggregating the text for **multiple proofs** steps into a single sentence when the steps
www.cs.columbia.edu/~regina/my_papers/proofs.ps.gz

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as our scheme is based on oblivious decision **proofs**, **multiple** ownership problem (image to be owned more
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geometry theorem prover, as well as visualise **multiple proofs** to single theorems. We discuss the features
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actually tends to favor conditions having **multiple proofs**, which seems harmful to utility since many
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called 'spurious ambiguity' i.e. allowing **multiple proofs** which assign the same reading for some
acl.ldc.upenn.edu/P/P97/P97-1044.pdf

[Abductive Theorem Proving for Analyzing Student Explanations - Jordan, Makatchev, VanLehn \(2003\)](#) (Correct)

the student's intended meaning when there are **multiple proofs**. First we explain the pedagogical
 proofs it creates. However there can still be **multiple proofs** that are considered equally good
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[Abductive Theorem Proving for Analyzing Student.. - Makatchev, Jordan.. \(2003\)](#) (Correct)

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 under two conditions. First, there may be **multiple proofs** that are equally good. This condition
andes1.lrdc.pitt.edu/~maxim/papers/arte.ps.gz

[Extended Explanations as Student Models for Guiding.. - Jordan, Makatchev.. \(2003\)](#) (Correct)

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[Automatic Acquisition of Search Control Knowledge from.. - Denzinger, Schulz \(2000\)](#) (Correct)

Acquisition of Search Control Knowledge from **Multiple Proof** Attempts Jorg Denzinger 1 and Stephan

for another target example. Our approach uses **multiple proofs** for different problems. 2.3 The Teamwork
www.jessen.informatik.tu-muenchen.de/~schulz/PAPERS/DS97-JICS.ps.gz

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[PS] Learning Domain Knowledge to Improve Theorem Proving

J Denzinger, S Schulz - CADE, 1996 - informatik.tu-muenchen.de

... be used as an indicator for its versatility (ie its ability to contribute to **multiple proofs**), while the ... The rst set contains patterns organized by **theorem**. ...

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Social and Semiotic Analyses for Theorem Prover User Interface Design 1

J Goguen - Formal Aspects of Computing, 1999 - springerlink.com

... Distributed cooperative proving over the internet, allowing **multiple proofs** for (sub)goals ... Social and Semiotic Analyses for **Theorem Prover User Interface** 281 ...

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Verbalization of high-level formal proofs

AM Holland-Minkley, R Barzilay, RL Constable - AAAI/IAAI, 1999 - cs.cornell.edu

... The generation system for the Coq **theorem prover** takes the approach of generating text for every step but aggregating the text for **multiple proofs** steps into a ...

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Non-malleable non-interactive zero knowledge and adaptive chosen-ciphertext security

A Sahai - FOCS, 1999 - ieeeexplore.ieee.org

... We first define efficient non-interactive proof sys- tems, and then give a definition of adaptive single- **theorem** non-interactive zero-knowledge (as in [FLS]): ...

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Theorem Proving for Untyped Constructive λ -Calculus: Implementation and Application

A Ramsay - Logic Journal of the IGPL, 2001 - oup.co.uk

... 94 **Theorem** proving for untyped constructive λ -calculus ... ground at the point when it is called, we know that there is no point in finding **multiple proofs** of it. ...

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[PS] Automatic Acquisition of Search Control Knowledge from Multiple Proof Attempts

J Denzinger, S Schulz - Information and Computation, 2000 - informatik.tu-muenchen.de

... extensions of it provide a good basis for learning approaches to control the search of a **theorem prover** ... Our approach uses **multiple proofs** for diereent prob- lems ...

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Muse - A Computer Assisted Verification System

JD Halpern, S Owre, N Proctor, WF Wilson, I Sytek - IEEE Transactions on Software Engineering, 1987 - doi.ieeecomputersociety.org

... This naming facility was exploited mostly to develop proof frag- ments that were used as parts of **multiple proofs**. The **Theorem Prover** section con- tains a more ...

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[PS] Term Space Mapping for DISCOUNT

S Schulz - Proc. of the CADE-15 Workshop on Using AI methods in ..., 1998 - informatik.tu-muenchen.de

... Finally, the generalized equations from **multiple proofs** are compiled into a term space map, which is used in the main **theorem prover** to evaluate facts during ...

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[PS] Lemma Usage in Connection Method Theorem Proving

G Neugebauer - 1990 - intellektik.informatik.tu-darmstadt.de

... 6 Conclusion We have shown a method to use lemmas and pending subgoals inside a connection method **theorem** prover to avoid **multiple proofs** of the same literal. ...

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Inference-Security Analysis Using Resolution **Theorem**-Proving

NC Rowe - ICDE, 1989 - cs.nps.navy.mil

... Resolution **theorem**-prover with ... inferred $\text{clause2}(C, T, C2, T2) :- \text{subset}(C, C2), \text{not}(T=T2), \text{interval_overlap}(T, T2, T3), \text{write}(\text{'Multiple proofs found of clause ...$

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Fast Verification of Any Remote Procedure Call: Short Witness-Indistinguishable One-Round Proofs for ...

W Aiello, SN Bhatt, R Ostrovsky, S Rajagopalan - ICALP, 2000 - springerlink.com

... Our result also has interesting connection to the PCP **theorem** [2]. Recall that in the PCP settings Prover must send to the Verifier an entire proof. ...

Cited by 9 - [Web Search](#) - portal.acm.org - eprint.iacr.org

Promoting appropriate uses of technology in mathematics teacher preparation

J Garofalo, H Drier, S Harper, MA Timmerman, T ... - Contemporary Issues in Technology and Teacher Education, 2000 - citejournal.org

... representations of the mathematical structure of the Pythagorean **theorem**. ... interconnect mathematics topics, write programs, devise **multiple proofs** and solutions ...

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Domain-independent task specification in the TACITUS natural language system

M Tyson, JR Hobbs, AI Center - 1990 - isi.edu

... At each iteration in one of the inner loops, the **theorem** prover checks to see ... If **multiple proofs** have been found, the analysis module is given multiple sets of ...

Cited by 1 - [View as HTML](#) - [Web Search](#) - isi.edu - [Library Search](#)

[PS] An Overview of the Tecton Proof System

D Kapur, X Nie, DR Musser - Theoretical Computer Science, 1994 - cs.albany.edu

... inference mechanisms used to prove rst-order formulas arising in specification analysis and in building theories, and the integration into the **theorem** prover of ...

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Maximal Incrementality in Linear Categorical Deduction

M Hepple - ACL, 1997 - acl.lidc.upenn.edu

... Approaches within this framework employ a **theorem** proving method that ... equivalence', also called 'spurious ambiguity', ie allowing **multiple proofs** which assign ...

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A Practical Approach to Security Assessment

DM Kienzie, WA Wulf - Proceedings of the 1997 New Security Paradigms Workshop, 1997 - portal.acm.org

... Even in mathematics, we often see **multiple proofs** of the same **theorem**-because proofs, like MOATS, are not intended to "prove" but rather to convince the ...

Cited by 9 - [Web Search](#) - seclab.cs.ucdavis.edu - cs.virginia.edu - portal.acm.org

Single axioms for groups and Abelian groups with various operations

W McCune - Journal of Automated Reasoning, 1993 - springerlink.com

... than as a multiliteral clause and marked success with **multiple proofs** rather than with ... Aside from automated **theorem** proving, OTTER can also serve as a symbolic ...

Cited by 28 - [Web Search](#) - mirror.mcs.anl.gov - info.mcs.anl.gov

Hardness of approximate hypergraph coloring

V Guruswami, J Haastad, M Sudan - Electronic Colloquium on Computational Complexity (ECCC), 2000 - epubs.siam.org

... r, q)-restricted if, on input x (implying the **theorem** $x \in L$... the probability with which the verifier accepts a given input, here we allow **multiple proofs** to be ...

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[PS] [The Generality of Overgenerality](#)

WW Cohen - ML, 1991 - [www-2.cs.cmu.edu](#)

... used, but as in learning relational concepts, the presence of **multiple proofs** means that ... enumerating the above list of rules is to modify the **theorem** prover to ...

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[An iterative rounding 2-approximation algorithm for the element connectivity problem](#)

L Fleischer, K Jain, DP Williamson - ANNU SYMP FOUND COMPUT SCI PROC. pp. 339-347. 2001, 2001 - [ieeexplore.ieee.org](#)

... the same connected component of G-X. The constraints of our integer program are based on a **theorem** of Menger (for **multiple proofs** and references, see [5]). ...

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Solving Open Questions and Other Challenge Problems Using Proof Sketches

R Veroff - Journal of Automated Reasoning, 2001 - kluweronline.com

... If we have **multiple proof** sketches for a target theorem, we may consider each to provide potentially valuable guidance and can consider them simultaneously. ...

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Automated generation of readable proofs with geometric invariants

SC Chou, XS Gao, JZ Zhang - Journal of Automated Reasoning, 1996 - springerlink.com

... This is the basis for **multiple proof** generation. ... Most of the previous work of **automated theorem** proving generally satisfy with one proof. ...

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A Survey of Automated Deduction

A Bundy - Artificial Intelligence Today, 1999 - springerlink.com

Page 1. A Survey of Automated Deduction * Alan Bundy Division of Informatics

The University of Edinburgh Edinburgh EH1 2QL Abstract. ...

Cited by 8 - [Web Search](#) - [inf.ed.ac.uk](#) - [ags.uni-sb.de](#) - [informatics.ed.ac.uk](#) - [all 6 versions »](#)

[PS] Solving Open Questions and Other Challenge Problems Using Proof Sketches

R Vero - J. Automated Reasoning, 2001 - cs.unm.edu

... If we have **multiple proof** sketches for a target theorem, we may consider each to provide potentially valuable guidance and can consider them simultaneously. ...

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[PS] The Boyer-Moore theorem prover and its interactive enhancement

RS Boyer, M Kaufmann - Computers and Mathematics with Applications, 1995 - cs.utexas.edu

... abbreviation and comment mechanisms commands for undoing and restoring (undoing the undoing) support for saving **multiple proof** contexts ...

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The PROSPER toolkit

LA Dennis, G Collins, M Norrish, RJ Boulton, K ... - TACAS, 2000 - springerlink.com

Page 1. Int J Softw Tools Technol Transfer (2003) 4: 189–210 / Digital Object

Identifier (DOI) 10.1007/s100090200076 The P ROSPER toolkit ...

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A taxonomy of parallel strategies for deduction

MP Bonacina - Annals of Mathematics and Artificial Intelligence, 2000 - kluweronline.com

Page 1. Annals of Mathematics and Artificial Intelligence 29: 223–257, 2000. ©

2001 Kluwer Academic Publishers. Printed in the Netherlands. ...

Cited by 14 - [Web Search](#) - [springerlink.com](#) - [cs.uiowa.edu](#) - [sci.univr.it](#) - [all 6 versions »](#)

Reasoning Theories

F Giunchiglia, P Pecchiari, CL Talcott - Journal of Automated Reasoning, 2001 - springerlink.com

... forward rule composition (à la tactics; see, eg, (Gordon et al., 1979)), but also

all the usual tricks and annotations used in **automated theorem** provers, for ...

Cited by 53 - [Web Search](#) - [kluweronline.com](#) - [steam.stanford.edu](#) - [portal.acm.org](#) - [all 9 versions »](#)

[book] Formalized mathematics

J Harrison, A Reasoning - 1996 - crest.abo.fi

Page 1. Formalized Mathematics John Harrison Turku Centre for Computer Science TUCS

Technical Report No 36 August 1996 ISBN 951-650-813-8 ISSN 1239-1891 Page 2. ...

[Cited by 21](#) - [View as HTML](#) - [Web Search](#) - [cl.cam.ac.uk](#) - [rbjones.com](#) - [all 4 versions »](#) - [Library Search](#)

[BOOK] New Foundations for Automation of Default Reasoning

T Linke - 2000 - [print.google.com](#)

Page 1. DISK! 237 I II iIUNiIIIIIIHIUI 45E6-4KD-N1P2 Page 2. DISK!

Dissertationen zur KUnstlichen Intelligenz Mit Unterstutzung des ...

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[PS] Formalized Mathematics (DRAFT)

J Harrison - Available from the author at jharriso@abo.fi or [http://www ...](http://www...), 1996 - [cl.cam.ac.uk](#)

Page 1. Formalized Mathematics (DRAFT) John Harrison Abo Akademi University,

Department of Computer Science Lemminkaisenkatu 14a, 20520 ...

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trace "distance measure"

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The design of the seer predictive caching system

G Kuenning, GJ Popek - Proc. of IEEE Workshop on Mobile Computing Systems & ..., 1994 - ieeexplore.ieee.org

... the user's se- quence of file accesses as the basis of our **distance measure**. ... is the appearance of utility pro- grams, such as lbm/cc, in the reference **trace**. ...

Cited by 48 - [Web Search](#) - cs.ucla.edu - tcos.org - cs.odu.edu - [all 5 versions »](#)

Quantitative comparison of classification capability: Fully polarimetric versus dual and single-

JS Lee, MR Grunes, E Pottier - IEEE Transactions on Geoscience and Remote Sensing, 2001 - ieeexplore.ieee.org

... where dimension of the vector ; reciprocal case; bistatic case; **trace** of a ... Data Classifier For terrain or land-use classification, a **distance measure** [13] was ...

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Quantification of injury-related EEG signal changes using Itakura **distance measure**

X Kong, V Goel, N Thakor - Acoustics, Speech, and Signal Processing, 1995. ICASSP-95., ..., 1995 - ieeexplore.ieee.org

... set a models the test signal by calculating aTRcx (6) Combining (3) and (6) we have a symmetric **distance measure** di(r ... Each **trace** has a duration of five seconds. ...

Cited by 4 - [Web Search](#) - ieeexplore.ieee.org

Transcribing Radio News

F Kubala, T Anastasakos, H Jin, L Nguyen, R ... - Proceedings of ICSLP-96, Philadelphia PA, Oct, 1996 - ieeexplore.ieee.org

... The noisy **trace** is the value of the **distance measure** computed at each frame position in a 214 second segment containing 9 speaker turns. ...

Cited by 7 - [Web Search](#) - ieeexplore.ieee.org

[PS] Fast Non-Linear Dimension Reduction

N Kambhatla, TK Leen - NIPS, 1993 - archive.cis.ohio-state.edu

... of the representation layer are swept, the outputs **trace** out the ... The modied VQPCA algorithm (with the reconstruction **distance measure** used for clustering ...

Cited by 53 - [View as HTML](#) - [Web Search](#) - cs.cuhk.hk - neural.cse.ogi.edu - funet.fi - [all 7 versions »](#)

Reliable feature matching across widely separated views

A Baumberg - CVPR, 2000 - doi.ieeeecomputersociety.org

... at some scale t and is the image convolution operator over $x^2 < 2$. A Harris corner strength measure is calculated from the determinant and the **trace** of this ...

Cited by 122 - [Web Search](#) - doi.ieeeecs.org - ieeexplore.ieee.org - cre.canon.co.uk - [all 10 versions »](#)

A Data-Driven Method for Choosing Smoothing Parameters in Optical Flow Problems

L Ng, V Solo - The 1997 International Conference on Image Processing. Part ..., 1997 - doi.ieeeecomputersociety.org

... For our purpose, we modify the method described in [9] by using W as the weight in the **distance measure** instead of the blurring operator. ... n^2 **trace** , WA , $1 W T$...

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A Program for Aligning Sentences in Bilingual Corpora

WA Gale, KW Church - Computational Linguistics, 1994 - portal.acm.org

... innovation into the structure of the **distance measure**. Kruskal and Liberman (1983) describe distance measures as belonging to one of two classes: **trace** and time ...

Cited by 355 - [Web Search](#) - citeseer.csail.mit.edu - aclweb.org - cs.mu.oz.au - [all 24 versions »](#)

Distance-based redundancy analysis: Testing multispecies responses in multifactorial ecological ...

P Legendre, MJ Anderson - Ecological Monographs, 1999 - bio.umontreal.ca

... Steps in the procedure include: (1) calculating a matrix of distances among replicates using a **distance measure** of choice (eg, Bray-Curtis); (2) determining ...

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Feature Comparisons of 3-D Vector Fields Using Earth Mover's Distance

R Batra, L Hesselink, CA Stanford, CA Stanford - IEEE Visualization, 1999 - ieeexplore.ieee.org

... work, rethinking the representation of a critical point signature and the **distance measure** between ... where $P = -\text{trace}(J)$ and $Q = \det(J)$ is used to classify ...

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[PS] Distance measures in terms of substitution processesE Baake, A von Haeseler - Theor. Popul. Biol, 1999 - zi.biologie.uni-muenchen.de... any tree-additive pair-wise **distance measure**. 2 Introduction ... R and S. Since commutators have vanishing **trace**, we may note, for later use: ...Cited by 9 - [View as HTML](#) - [Web Search](#) - techfak.uni-bielefeld.de - ingentaconnect.com - [all 4 versions](#) »Entanglement measures and purification proceduresV Vedral, MB Plenio - Physical Review A, 1998 - dx.doi.org... and the **proof** is completed. The physical explanation ... 2. Bures metric Another **distance measure** that leads ... increase under a complete positive **trace** preserving map ...Cited by 264 - [Web Search](#) - arxiv.org - vlatko.madetomeasure.biz - puhep1.princeton.edu - [all 9 versions](#) »Subspace angles and distances between ARMA modelsK De Cock, B De Moor - Proc. of Mathematical Theory of Networks and Systems, 2000 - univ-perp.fr... Keywords: principal angles, ARMA models, linear systems, stochastic realization, **distance measure** ... and U^2 are the principal directions (for a **proof** see eg ...)Cited by 14 - [View as HTML](#) - [Web Search](#) - esat.kuleuven.ac.beA nearest hyperrectangle learning methodS Salzberg - Machine Learning, 1991 - springerlink.com... This **proof** of this result is via a reduction to a ... As we **trace** out the surface, we find it alters ... hole in the outer one, so the **distance measure** does not ...Cited by 191 - [Web Search](#) - kluweronline.com - ingentaconnect.com - portal.acm.org - [all 7 versions](#) »Binomial mixtures: geometric estimation of the mixing distributionGR Wood - Ann. Statist, 1999 - projecteuclid.org... p runs from 0 to 1 such points **trace** out the ... as we smooth it, using a **distance measure**, to a ... hinge on the following fact, demonstrated in [27], **proof** of The ...Cited by 2 - [Web Search](#) - www-ist.massey.ac.nz - projecteuclid.orgPassive velocity field control of mechanical manipulatorsPY Li, R Horowitz - IEEE Transactions on Robotics and Automation, 1999 - ieeexplore.ieee.org... where 0 is a constant and is the gradient vector field of the **distance measure** with respect to while keeping constant (see [3] for ... **Proof**: Notice that, since ...)Cited by 48 - [Web Search](#) - me.umn.edu - me.umn.edu - dx.doi.org - [all 5 versions](#) »s. N. G. cnuF REN - ELECTRONICS LETTERS, 1991 - ieeexplore.ieee.org... mV output with twice frequency (lower **trace**) Acknowledgment: One ... INSIGHT INTO OPERATION OF KULPA BOUNDARY **DISTANCE MEASURE** ... is valid, though the **proof** is purely ...[Web Search](#)M. You and AKC Wong, "An algorithm for graph optimal isomor-HA Sneath, RR Sokal, N Taxonomy, S Francisco, BA ... - Genetics, 1969 - doi.ieeecomputersociety.org... where tr denotes the **trace** of the matrix and det ... the full divergence measure, a similar **proof** is given ... compared with the Itakura-Saito **distance measure** for a ...[Web Search](#) - doi.ieeecs.orgModeling the covarion hypothesis of nucleotide substitutionC Tuffley, M Steel, N Zealand - Math. Biosci, 1998 - math.canterbury.ac.nz... follows from an argument similar to the **proof** of Bernstein ... across-sites model if only the **trace** of the ... based on the construction of a **distance measure** which is ...

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Minimum Spanning Trees for Gene Expression Data Clustering

Y Xu, V Olman, D Xu - Proceedings of the 12th International Conference on Genome ..., 2001 - hc.ims.u-tokyo.ac.jp

... We omit the detailed **proof**. ... minimum value, we need some simple bookkeeping to **trace** back which ... on a user's selection of the **distance measure**, the program ...

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